



SGM8270-4Q

Low Noise, Precision, High Voltage, Rail-to-Rail I/O, Automotive Operational Amplifier

GENERAL DESCRIPTION

The SGM8270-4Q is a quad, low noise, precision operational amplifier, optimized for high voltage operation from 3.3V to 36V single supply or $\pm 1.65\text{V}$ to $\pm 18\text{V}$ dual power supplies. It provides rail-to-rail input with a wide input common mode voltage range and rail-to-rail output voltage swing.

Furthermore, the SGM8270-4Q provides high slew rate, low noise, low offset current and voltage, and low bias current.

The device is AEC-Q100 qualified (Automotive Electronics Council (AEC) standard Q100 Grade 1) and it is suitable for automotive applications.

The SGM8270-4Q is available in Green SOIC-14 and TSSOP-14 packages. It is specified over the extended -40°C to $+125^{\circ}\text{C}$ temperature range.

FEATURES

- AEC-Q100 Qualified for Automotive Applications
Device Temperature Grade 1
 $T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$
- Rail-to-Rail Input and Output
- Wide Input Common Mode and Differential Voltage Ranges
- Low Input Offset Voltage: $\pm 1.4\text{mV}$ (MAX)
- Low Input Bias Current
- Low Input Offset Current
- High Input Impedance
- Output Short-Circuit Protection
- Low Noise: $15\text{nV}/\sqrt{\text{Hz}}$ at 1kHz
- Gain-Bandwidth Product: 2.5MHz
- High Slew Rate: $8\text{V}/\mu\text{s}$
- Available in Green SOIC-14 and TSSOP-14 Packages

APPLICATIONS

Automotive Application
High Impedance Sensor
Photodiode Amplifier
Automotive Audio
DAC Output Amplifier

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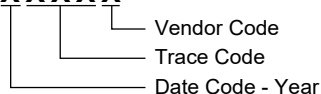
PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE TOP MARKING	PACKING OPTION
SGM8270-4Q	SOIC-14	-40°C to +125°C	SGM8270-4QS14G/TR	1YDS14 XXXXX	Tape and Reel, 2500
	TSSOP-14	-40°C to +125°C	SGM8270-4QTS14G/TR	1YE TS14 XXXXX	Tape and Reel, 4000

MARKING INFORMATION

NOTE: XXXXX = Date Code, Trace Code and Vendor Code.

XXXXX



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS ⁽¹⁾

Supply Voltage, +V _S to -V _S	40V
Differential Input Voltage, V _{ID} 	(+V _S) - (-V _S)
Input/Output Voltage Range.....	(-V _S) - 0.3V to (+V _S) + 0.3V
Power Dissipation, P _D @ T _A = +25°C	
SOIC-14	1.5W
TSSOP-14	1.3W
Package Thermal Resistance	
SOIC-14, θ _{JA}	78.4°C/W
SOIC-14, θ _{JB}	35.1°C/W
SOIC-14, θ _{JC}	38.7°C/W
TSSOP-14, θ _{JA}	93.9°C/W
TSSOP-14, θ _{JB}	56.7°C/W
TSSOP-14, θ _{JC}	32.6°C/W
Junction Temperature.....	+150°C
Storage Temperature Range.....	-65°C to +150°C
Lead Temperature (Soldering, 10s).....	+260°C
ESD Susceptibility ^{(2) (3)}	
HBM.....	±8000V
CDM	±1000V

NOTES:

1. It is recommended that CMOS device adopts the proper power supply sequence. Always sort the V_S first, followed by the inputs and outputs.
2. For human body model (HBM), all pins comply with AEC-Q100-002 specification.
3. For charged device model (CDM), all pins comply with AEC-Q100-011 specification.

RECOMMENDED OPERATING CONDITIONS

Operating Temperature Range.....	-40°C to +125°C
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OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

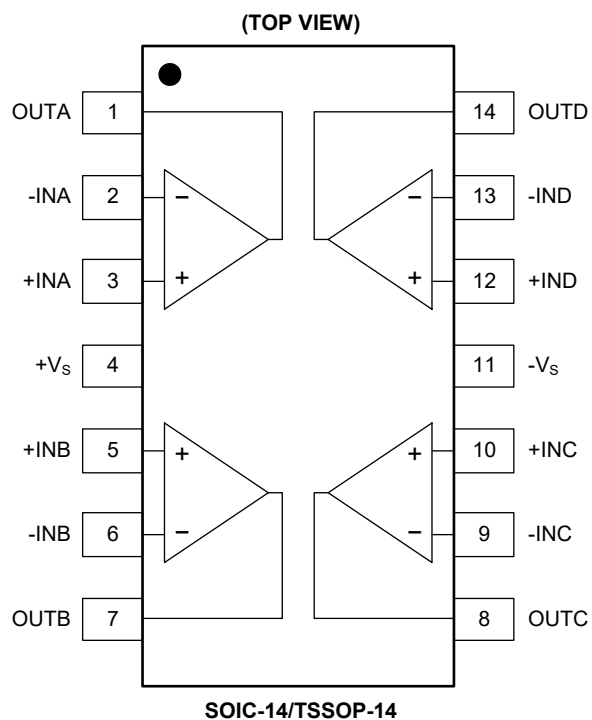
ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATIONS



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ELECTRICAL CHARACTERISTICS

($V_S = \pm 1.65V$ to $\pm 18V$ and $R_L = 2k\Omega$ connected to $0V$, Full = $-40^\circ C$ to $+125^\circ C$, typical values are at $T_A = +25^\circ C$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Input Characteristics							
Input Offset Voltage	V _{OS}	V _{CM} = 0V	+25°C		±0.2	±1.4	mV
			Full			±1.6	
Input Offset Voltage Drift	ΔV _{OS} /ΔT		Full		0.8		μV/°C
Input Bias Current	I _B	V _{CM} = 0V	+25°C		±10	±500	pA
			Full			±15	nA
Input Offset Current	I _{OS}	V _{CM} = 0V	+25°C		±10	±500	pA
			Full			±15	nA
Maximum Input Difference Bias Current	I _{ID}	V _S = ±18V, V _{ID} = ±18V	+25°C		2.6	3.5	μA
			Full			4.5	
Input Common Mode Voltage Range	V _{CM}		Full	(-V _S) - 0.1		(+V _S) + 0.1	V
Common Mode Rejection Ratio	CMRR	V _S = ±18V, (-V _S) - 0.1V < V _{CM} < (+V _S) - 1.5V	+25°C	96	110		dB
			Full	93			
		V _S = ±18V, (-V _S) - 0.1V < V _{CM} < (+V _S) + 0.1V	+25°C	80	94		
			Full	77			
Open-Loop Voltage Gain	A _{OL}	(-V _S) + 0.2V < V _{OUT} < (+V _S) - 0.2V, R _L = 10kΩ	+25°C	101	130		dB
			Full	98			
		(-V _S) + 0.6V < V _{OUT} < (+V _S) - 0.6V, R _L = 2kΩ	+25°C	101	123		
			Full	95			
Output Characteristics							
Output Voltage Swing from Rail	V _{OUT}	V _S = ±18V, R _L = 10kΩ	+25°C		65	100	mV
			Full			130	
		V _S = ±18V, R _L = 2kΩ	+25°C		320	450	
			Full			600	
Output Short-Circuit Current	I _{SC}	V _S = ±18V	+25°C	±18	±45		mA
Power Supply							
Operating Voltage Range	V _S		Full	3.3		36	V
Quiescent Current	I _Q	I _{OUT} = 0mA	+25°C		2.6	3.5	mA
			Full			4	
Power Supply Rejection Ratio	PSRR	V _S = 3.3V to 36V	+25°C	105	125		dB
			Full	102			

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ELECTRICAL CHARACTERISTICS (continued)

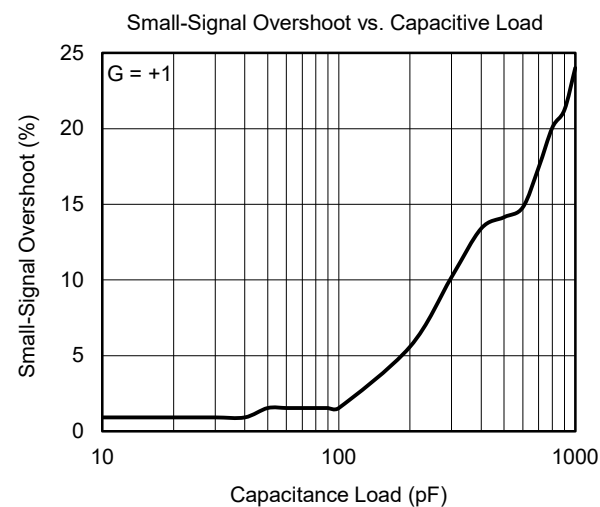
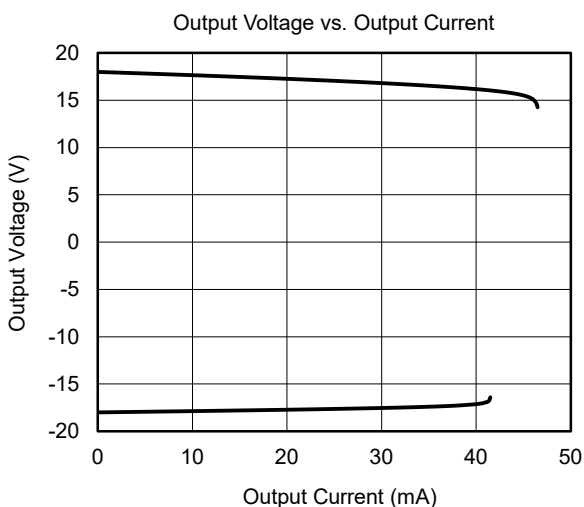
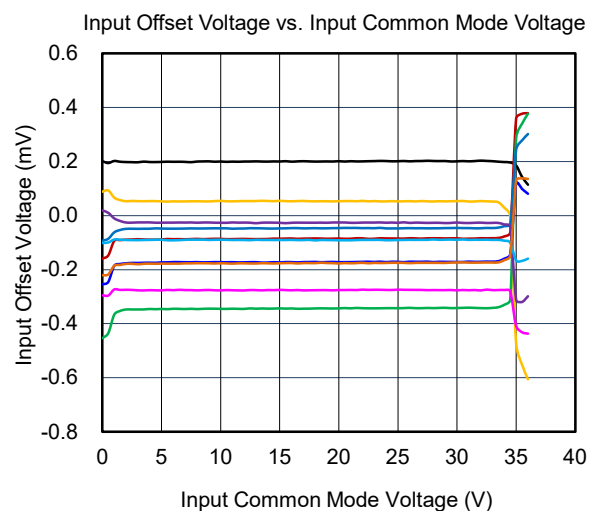
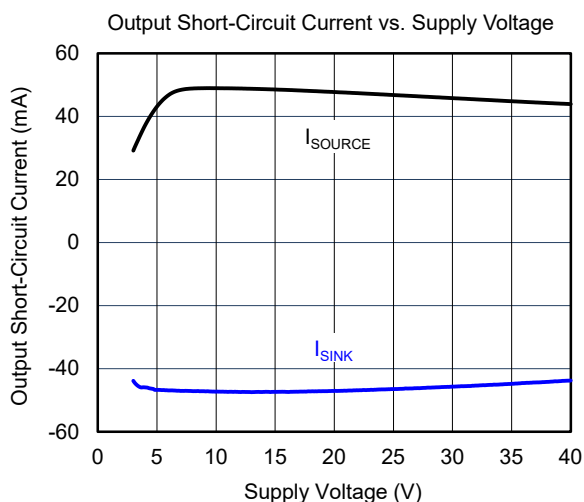
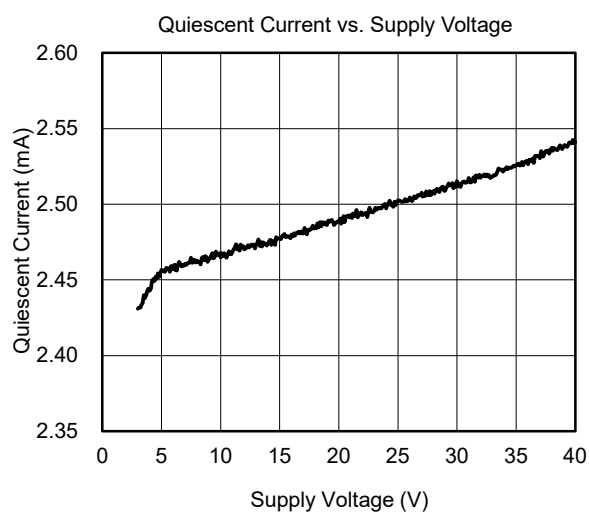
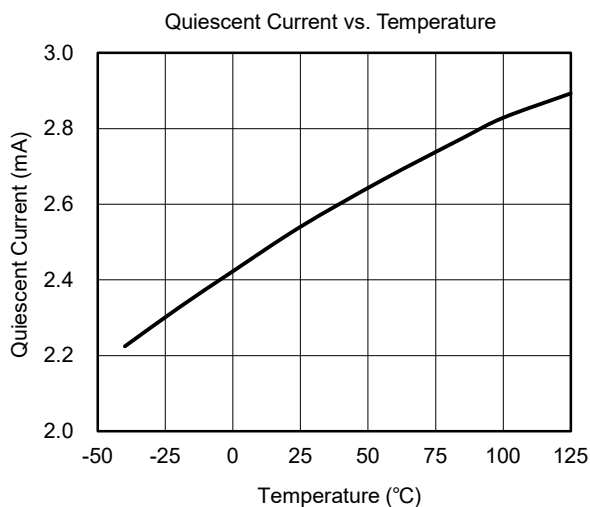
($V_S = \pm 1.65V$ to $\pm 18V$ and $R_L = 2k\Omega$ connected to $0V$, Full = $-40^\circ C$ to $+125^\circ C$, typical values are at $T_A = +25^\circ C$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Dynamic Performance							
Gain-Bandwidth Product	GBP	C _L = 50pF	+25°C		2.5		MHz
Phase Margin	φ _o	C _L = 50pF	+25°C		65		°
Slew Rate	SR	V _S = ±2.5V to ±18V, G = +1	+25°C		8		V/μs
Overload Recovery Time	ORT	V _{IN} × G > V _S	+25°C		1		μs
Total Harmonic Distortion + Noise	THD+N	V _S = ±2.5V to ±18V, V _{OUT} = 2V _{P-P} , f = 1kHz, G = +1, R _L = 600Ω	+25°C		0.002		%
		V _S = ±2.5V to ±18V, V _{OUT} = 2V _{P-P} , f = 1kHz, G = +1, R _L = 2kΩ	+25°C		0.0008		
Noise							
Input Voltage Noise		f = 0.1Hz to 10Hz	+25°C		4		μV _{P-P}
Input Voltage Noise Density	e _n	f = 10Hz	+25°C		80		nV/√Hz
		f = 1kHz	+25°C		15		
Input Current Noise Density	i _n	f = 1kHz	+25°C		300		fA/√Hz

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TYPICAL PERFORMANCE CHARACTERISTICS

At $T_A = +25^\circ\text{C}$, $V_S = \pm 18\text{V}$ and $R_L = 2\text{k}\Omega$, unless otherwise noted.



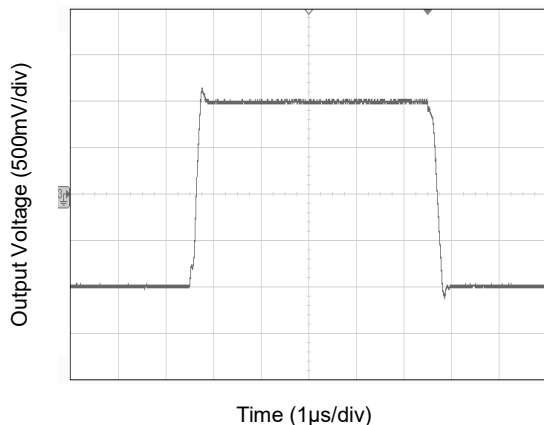
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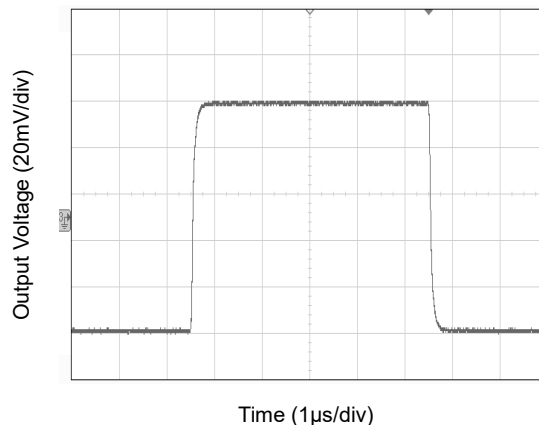
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

At $T_A = +25^\circ\text{C}$, $V_S = \pm 18\text{V}$ and $R_L = 2\text{k}\Omega$, unless otherwise noted.

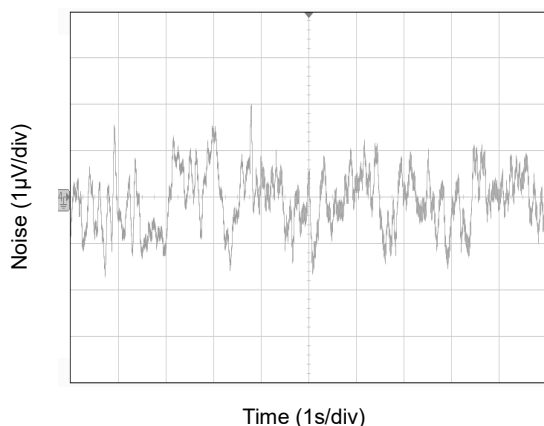
Large-Signal Step Response



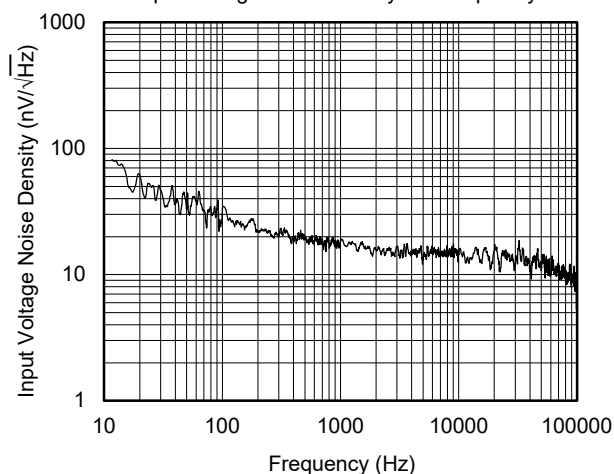
Small-Signal Step Response



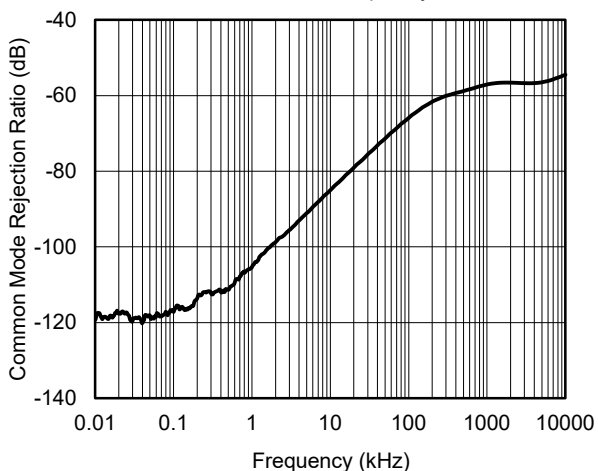
0.1Hz to 10Hz Input Voltage Noise



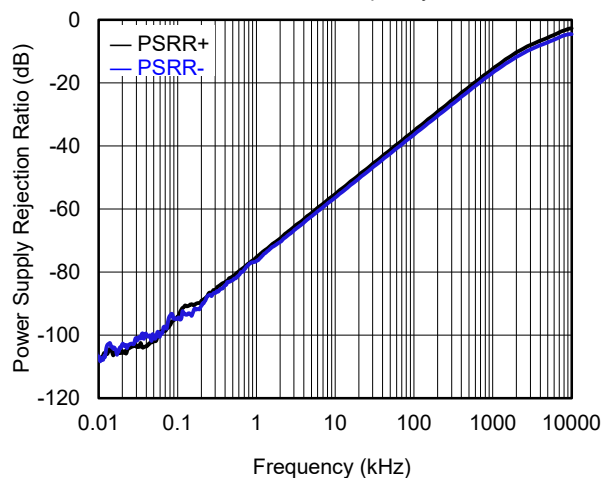
Input Voltage Noise Density vs. Frequency



CMRR vs. Frequency



PSRR vs. Frequency

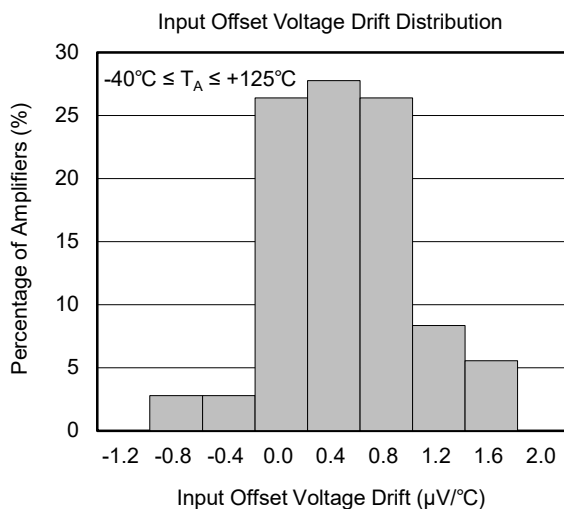
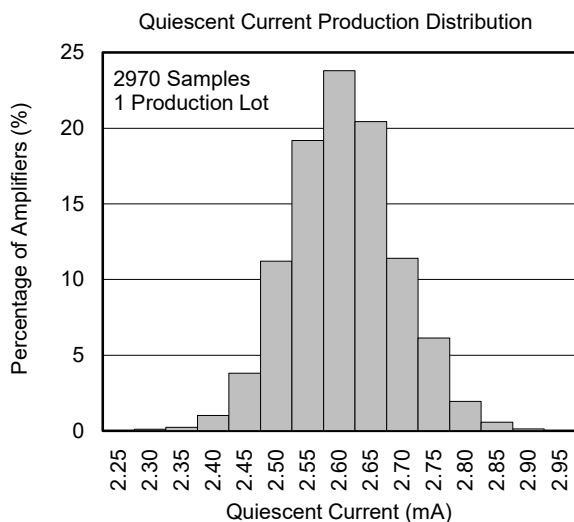
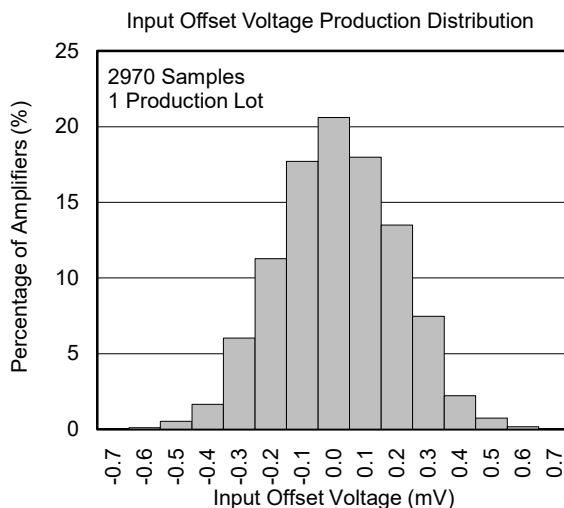
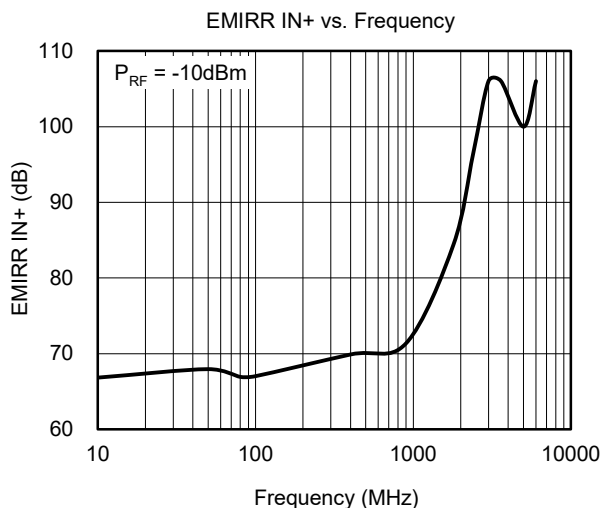
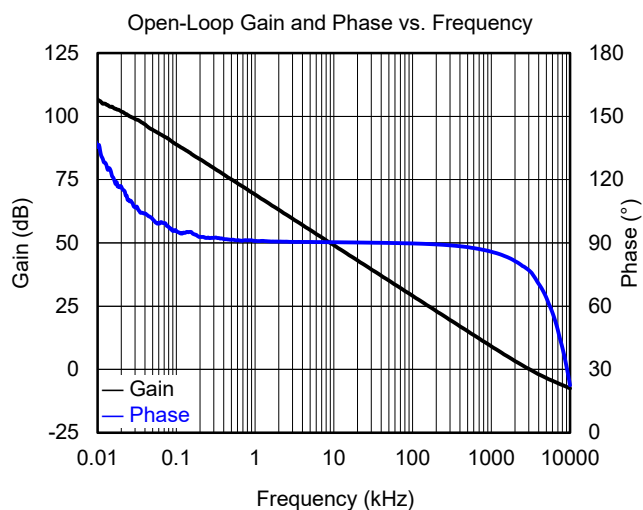
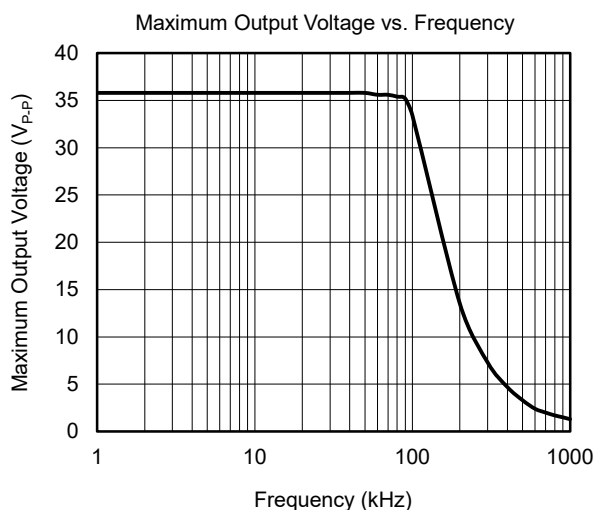


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TYPICAL PERFORMANCE CHARACTERISTICS (continued)

At $T_A = +25^\circ\text{C}$, $V_S = \pm 18\text{V}$ and $R_L = 2\text{k}\Omega$, unless otherwise noted.



REVISION HISTORY

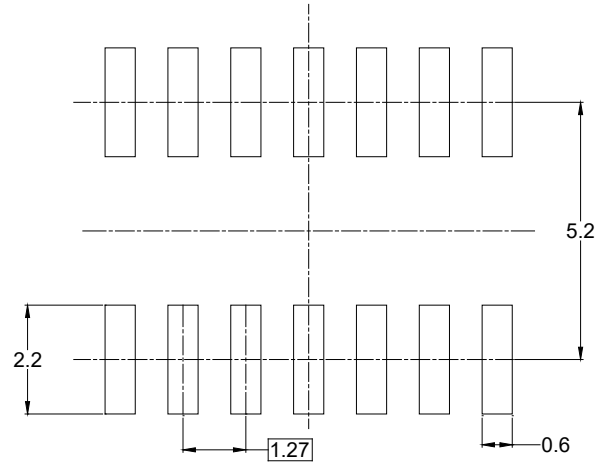
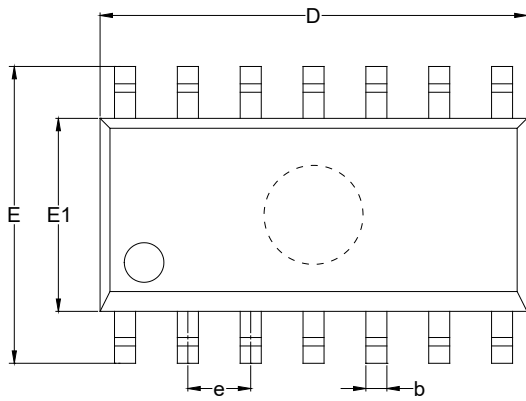
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Original to REV.A (DECEMBER 2025)	Page
Changed from product preview to production data.....	All

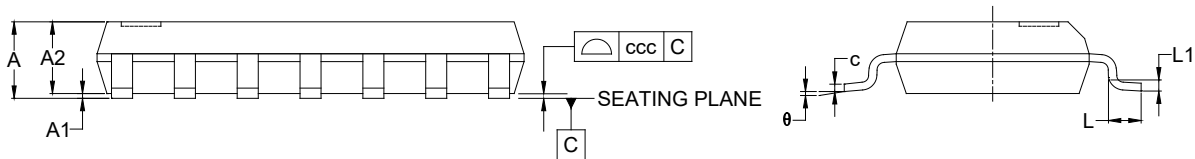
PACKAGE INFORMATION

PACKAGE OUTLINE DIMENSIONS

SOIC-14



RECOMMENDED LAND PATTERN (Unit: mm)



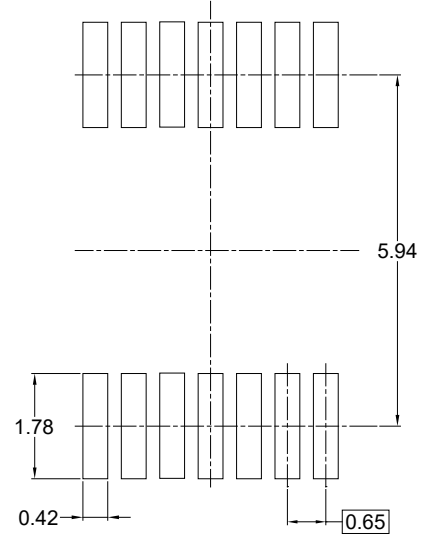
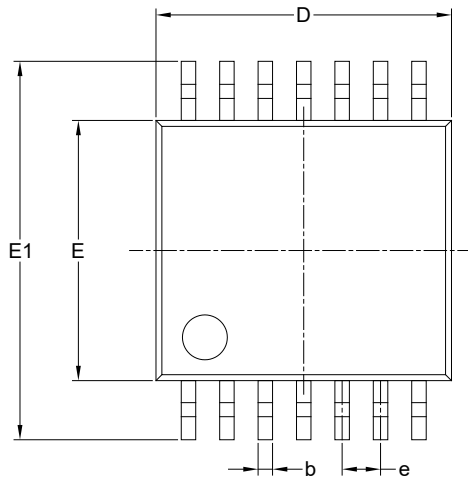
Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	-	-	1.750
A1	0.100	-	0.250
A2	1.250	-	-
b	0.310	-	0.510
c	0.100	-	0.250
D	8.450	-	8.850
E	5.800	-	6.200
E1	3.800	-	4.000
e	1.270 BSC		
L	0.400	-	1.270
L1	0.250 TYP		
θ	0°	-	8°
ccc	0.100		

NOTES:

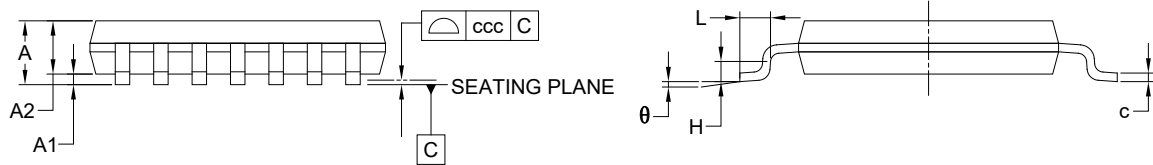
1. This drawing is subject to change without notice.
2. The dimensions do not include mold flashes, protrusions or gate burrs.
3. Reference JEDEC MS-012.

PACKAGE OUTLINE DIMENSIONS

TSSOP-14



RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A	-	-	1.200
A1	0.050	-	0.150
A2	0.800	-	1.050
b	0.190	-	0.300
c	0.090	-	0.200
D	4.860	-	5.100
E	4.300	-	4.500
E1	6.200	-	6.600
e	0.650 BSC		
L	0.450	-	0.750
H	0.250 TYP		
θ	0°	-	8°
ccc	0.100		

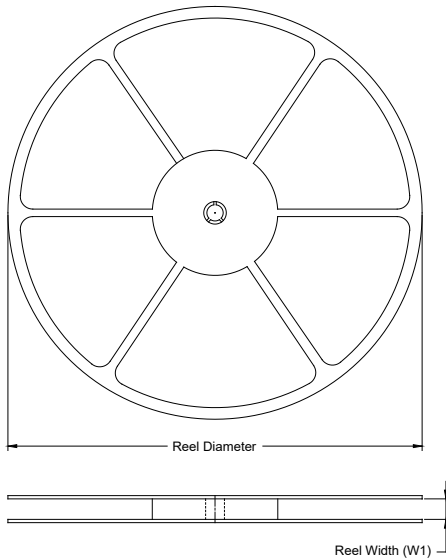
NOTES:

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3. Reference JEDEC MO-153.

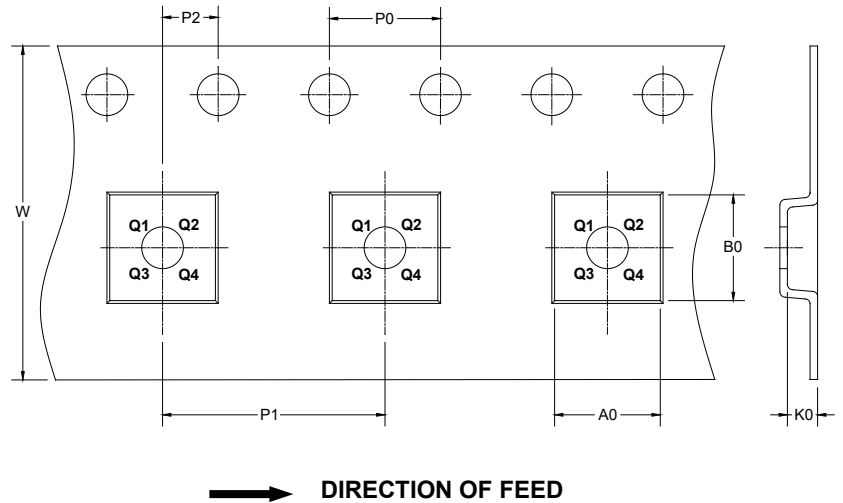
PACKAGE INFORMATION

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

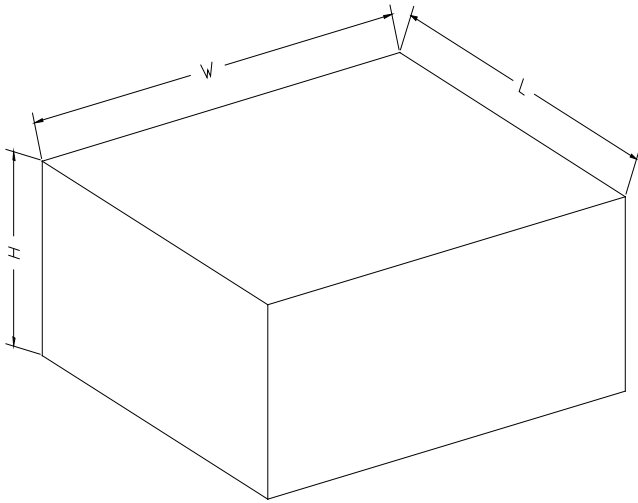
KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOIC-14	13"	16.4	6.60	9.30	2.10	4.0	8.0	2.0	16.0	Q1
TSSOP-14	13"	12.4	6.80	5.40	1.50	4.0	8.0	2.0	12.0	Q1

DD00001

PACKAGE INFORMATION

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13"	386	280	370	5

DD0002