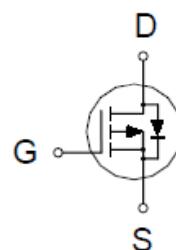
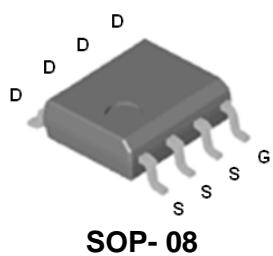


## P9006EVG

### P-Channel Enhancement Mode MOSFET

#### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
-60V	90mΩ @ $V_{GS} = -10V$	-4.5A



#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	$V_{DS}$	-60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current $T_A = 25^\circ C$	$I_D$	-4.5	A
$T_A = 70^\circ C$	$I_D$	-3.5	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	-20	
Power Dissipation $T_A = 25^\circ C$	$P_D$	2.5	W
$T_A = 70^\circ C$	$P_D$	1.6	
Operating Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C

#### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{JJA}$		50	°C / W

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Duty cycle  $\leq 1\%$ .

## P9006EVG

### P-Channel Enhancement Mode MOSFET

#### ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT	
			MIN	TYP	MAX		
<b>STATIC</b>							
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = -250\mu\text{A}$	-60			V	
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-1.0	-1.5	-2.5		
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 250$	nA	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -48V, V_{GS} = 0V$			1	$\mu\text{A}$	
		$V_{DS} = -36V, V_{GS} = 0V, T_J = 125^\circ\text{C}$			10		
On-State Drain Current <sup>1</sup>	$I_{D(\text{ON})}$	$V_{DS} = -5V, V_{GS} = -10V$	-20			A	
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(\text{ON})}$	$V_{GS} = -4.5V, I_D = -3.5\text{A}$		90	150	$\text{m}\Omega$	
		$V_{GS} = -10V, I_D = -4.5\text{A}$		70	90		
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = -10V, I_D = -4.5\text{A}$		9		S	
<b>DYNAMIC</b>							
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = -30V, f = 1\text{MHz}$		760		pF	
Output Capacitance	$C_{oss}$			90			
Reverse Transfer Capacitance	$C_{rss}$			40			
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DS} = 0.5V_{(\text{BR})\text{DSS}}, I_D = -4.5\text{A}, V_{GS} = -10V$		15.0		nC	
Gate-Source Charge <sup>2</sup>	$Q_{gs}$			2.5			
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$			3.0			
Turn-On Delay Time <sup>2</sup>	$t_{d(\text{on})}$	$V_{DS} = -20V, I_D \approx -1\text{A}, V_{GS} = -10V, R_{GS} = 6\Omega$		7	14	nS	
Rise Time <sup>2</sup>	$t_r$			10	20		
Turn-Off Delay Time <sup>2</sup>	$t_{d(\text{off})}$			19	34		
Fall Time <sup>2</sup>	$t_f$			12	22		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ\text{C}</math>)</b>							
Continuous Current	$I_S$				-1.3	A	
Pulsed Current <sup>3</sup>	$I_{SM}$				-2.6		
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = I_S, V_{GS} = 0V$			-1	V	
Reverse Recovery Time	$t_{rr}$	$I_F = -3.5\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		15.5		nS	
Reverse Recovery Charge	$Q_{rr}$			7.9		nC	

<sup>1</sup>Pulse test : Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

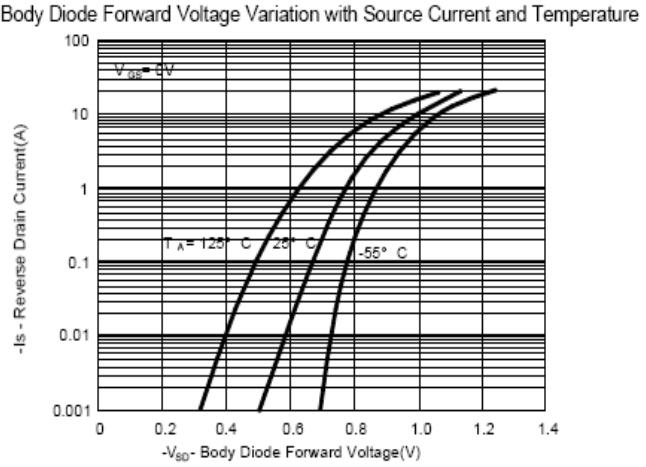
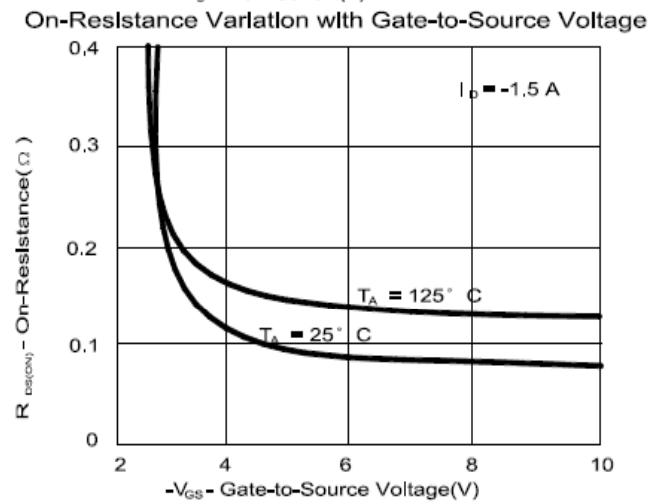
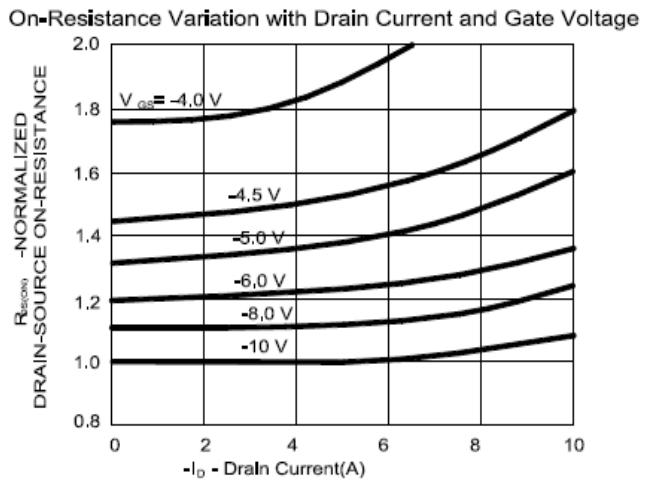
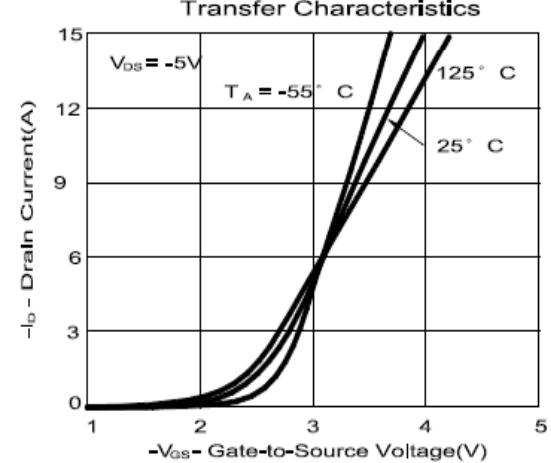
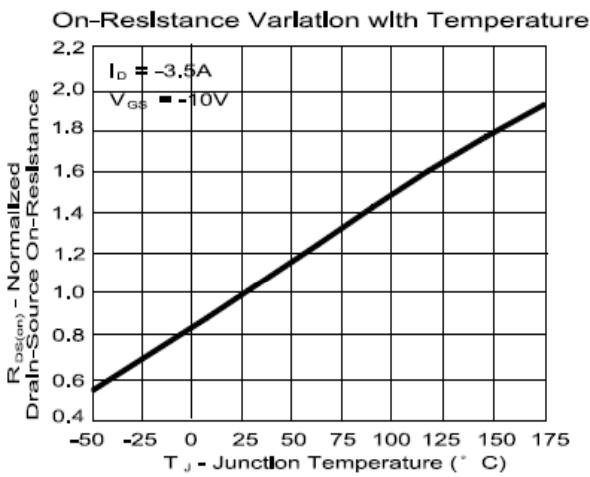
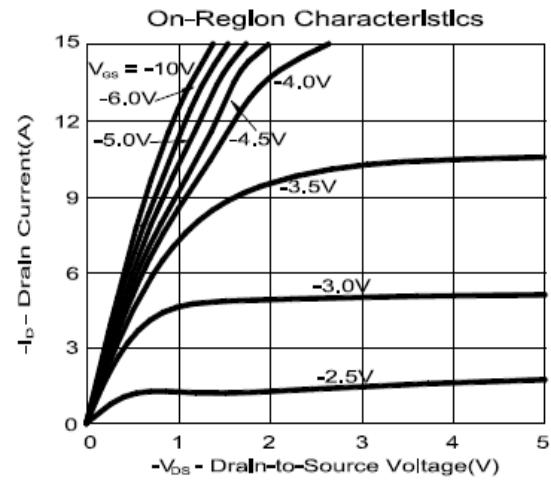
<sup>2</sup>Independent of operating temperature.

<sup>3</sup>Pulse width limited by maximum junction temperature.

## P9006EVG

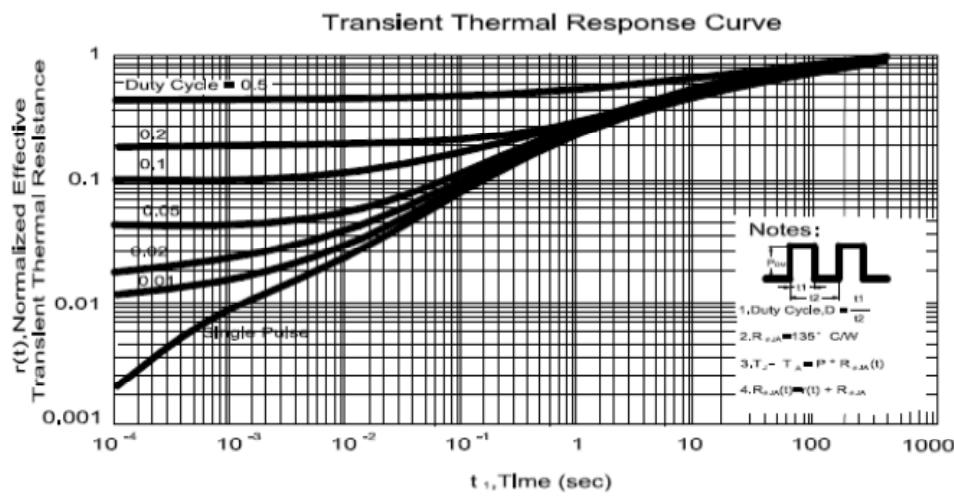
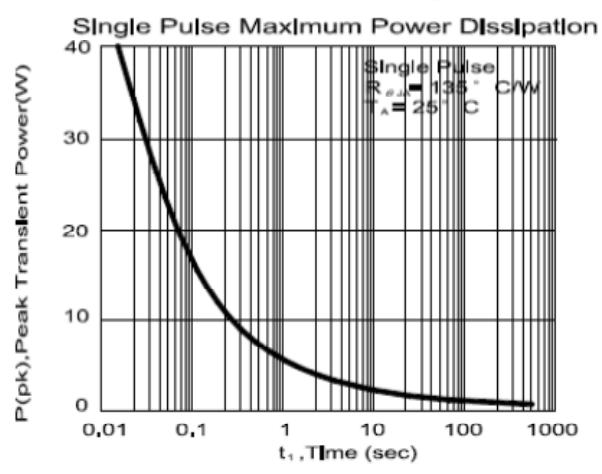
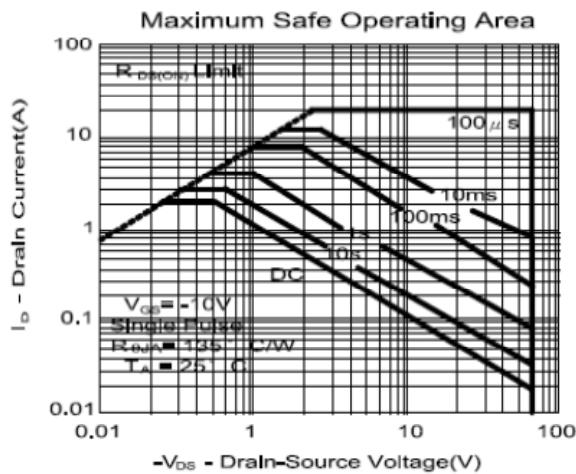
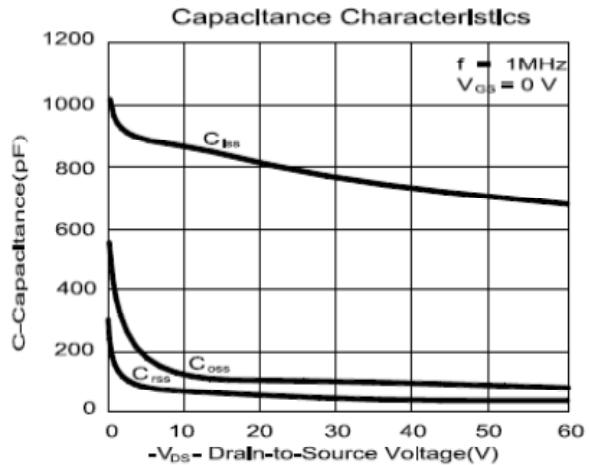
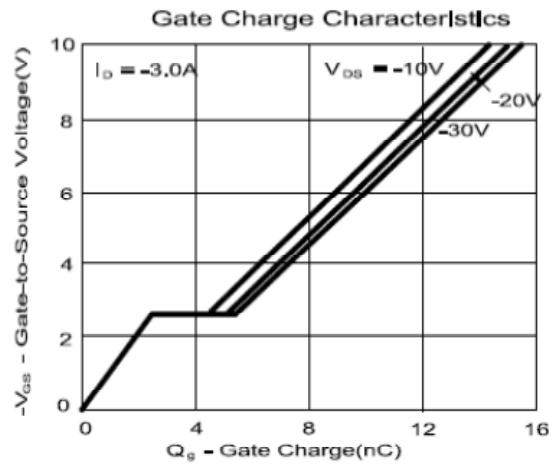
### P-Channel Enhancement Mode MOSFET

#### TYPICAL PERFORMANCE CHARACTERISTICS



## P9006EVG

### P-Channel Enhancement Mode MOSFET



## P9006EVG

### P-Channel Enhancement Mode MOSFET

#### Package Dimension

#### SOP-8 MECHANICAL DATA

Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.8	4.9	5.0	H	0.4	0.6	0.93
B	3.8	3.9	4.0	I	0.19	0.21	0.25
C	5.79	6.0	6.2	J	0.25	0.375	0.5
D	0.33	0.4	0.51	K	0°	3°	18°
E	1.25	1.27	1.29				
F	1.1	1.3	1.65				
G	0.05	0.15	0.25				

