

30V Output, 2.5A Current Limit, 1.2MHz High Efficiency Asynchronous Boost Regulator

FEATURES

- 2.5V to 5.5V Input Voltage Range
- Integrated 100mΩ Power MOSFET
- 1.2MHz Fixed Switching Frequency
- Internal 2.5A Switch Current Limit Typically
- Adjustable Output Voltage with 1.225V V_{FB}
- Internal Compensation
- Up to 30V Output Voltage
- Automatic Pulse Frequency Modulation Mode at Light Loads
- Available in a 5-Pin SOT23-5 Package

APPLICATIONS

- Portable Equipment Applications
- LED Application
- Digital Still and Video Cameras

GENERAL DESCRIPTION

The STI3508CB are constant frequency, peak current mode step-up converter with SOT23-5 package intended for small, low power applications. The STI3508CB switch at 1.2MHz and allow the use of tiny, low cost capacitors and inductors 2mm or less in height. Internal soft-start results in small inrush current and extends battery life.

The STI3508CB devices feature automatic shifting to pulse frequency modulation mode at light loads. The STI3508CB include undervoltage lockout, current limiting, and thermal overload protection to prevent damage in the event of an output overload. The STI3508CB are available in a small SOT23-5 package.

TYPICAL APPILCATION

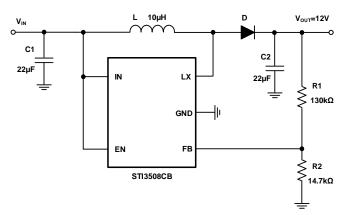
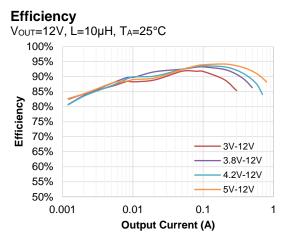


Figure 1. Basic Application Circuit of STI3508CB

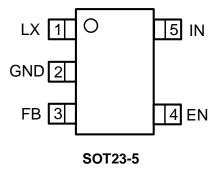




ABSOLUTE MAXIMUM RATINGS (Note 1)

Parameter	Min	Max	Unit
Input Supply Voltages	-0.3	7	V
LX Voltages	-0.3	35	V
EN, FB Voltage	-0.3	7	V
Storage Temperature Range	-	150	°C
Junction Temperature (Note 2)	-40	150	°C
Power Dissipation	-	600	mW
Lead Temperature Soldering,10Sec	-	260	°C

PIN CONFIGURATION



Top Mark: S35Exxx (S35E: Device Code, xxx: Inside Code) for STI3508CB

Part Number	Part Number Package		Quantity/ Reel	
STI3508CB	SOT23-5	S35Exxx	3000	

STI3508CB devices are Pb-free and RoHS compliant.



PIN FUNCTIONS

Pin	Name	Function
1 LX		Power Switch Output. LX is the drain of the internal MOSFET switch. Connect
		the power inductor and output rectifier to LX.
2	GND	Ground Pin
3	FB	Feedback Input. The FB voltage is 1.225V. Connect the resistor dividers to FB.
		Regulator On/Off Control Input. A high input at EN turns on the converter, and a
4	EN	low input turns it off. When not used, connect EN to the input supply for automatic
		startup.
5	IN	Input Supply Pin. Must be locally bypassed.

ESD RATING

Items	Description	Value	Unit
V_{ESD_HBM}	Human Body Model for all pins	±2000	V
V _{ESD_CDM}	Charge Device Model for all pins	±1000	V

JEDEC specification JS-001

RECOMMENDED OPERATING CONDITIONS

Items	Description	Min	Max	Unit
Input Voltage Range	V_{IN}	2.5	5.5	V
Output Voltage Range	V _{OUT}	V _{IN}	30	V
TJ	Operating Junction Temperature Range	-40	125	°C

THERMAL RESISITANCE (Note 3)

Items	Description	Value	Unit
θ_{JA}	Junction-to-ambient thermal resistance	200	°C/W
θ_{JC}	Junction-to-case thermal resistance	65	°C/W



ELECTRICAL CHARACTERISTICS

(VIN=VEN=3.7V, VOUT=12V, TA = 25°C, unless otherwise noted.)

Parameter	Test Conditions	Min	Тур	Max	Unit
Input Voltage Range		2.5		5.5	V
UVLO Threshold	V _{IN} Rising		2.4	2.48	V
UVLO Hysteresis			200		mV
Quiescent Current	V _{FB} =120%*V _{REF} , No switch		30	100	μΑ
Standby Current	$V_{IN}=V_{EN}=3.7V$, $I_{OUT}=0A$		450		μΑ
Shutdown Current	V _{EN} =0V		0.1	1	μΑ
Regulated Feedback Voltage	STI3508CB, T _A = 25°C	1.2	1.225	1.25	V
Oscillation Frequency	V _{OUT} =100%	1.0	1.2	1.4	MHz
On Resistance of LX			100		mΩ
Switching Current Limit			2.5		Α
EN Input Low Level				0.3	V
EN Input High Level		1.5			V
EN Leakage Current			0.1	1.0	μΑ
LX Leakage Current	V _{EN} =0V, V _{IN} =V _{LX} =25V		0.1	1.0	μΑ
Thermal Shutdown Threshold (Note 4)			160		°C
Thermal Shutdown Hysteresis (Note 4)			25		°C

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: T_J is calculated from the ambient temperature T_A and power dissipation P_D according to the following formula: $T_J = T_A + (P_D) \times \theta_{JA}$.

Note 3: Measured on JESD51-7, 4-layer PCB.

Note 4: Guaranteed by design.



FUNCTION DESCRIPTION

The STI3508CB uses a fixed frequency, peak current mode boost regulator architecture to regulate voltage at the feedback pin. The operation of the STI3508CB can be understood by referring to the block diagram of Figure 2. At the start of each oscillator cycle the MOSFET is turned on through the control circuitry. To prevent sub-harmonic oscillations at duty cycles greater than 50 percent, a stabilizing ramp is added to the output of the current sense amplifier and the result is fed into the negative input of the PWM comparator. When this voltage equals the output voltage of the error amplifier the power MOSFET is turned off. The voltage at the output of the error amplifier is an amplified version of the difference between the bandgap reference voltage and the feedback voltage. In this way the peak current level keeps the output in regulation. If the feedback voltage starts to drop, the output of the error amplifier increases. These results in more current to flow through the power MOSFET, thus increasing the power delivered to the output. The STI3508CB have internal soft start to limit the amount of input current at startup and to also limit the amount of overshoot on the output.

FUNCTIONAL BLOCK DIAGRAM

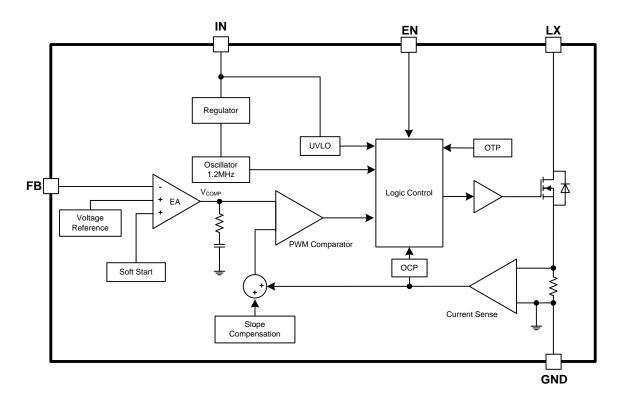


Figure 2. STI3508CB Block Diagram

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APPLICATION INFORMATION

Setting the Output Voltage

The internal reference V_{REF} is 1.225V. The output voltage is divided by a resistor divider, R1 and R2 to the FB pin. The output voltage is given by

$$V_{OUT} = V_{REF} \times \left(1 + \frac{R_1}{R_2}\right)$$

Inductor Selection

Inductor selection is important for ST3508CB applications. Inductor value determines input inductor current ripple value.

$$\Delta I_{L} = \frac{V_{IN} \times D}{f_{s} \times L} = \frac{V_{IN}}{f_{s} \times L} \times \left(1 - \frac{V_{IN} \times \eta}{V_{OUT}}\right)$$

Where:

D = duty cycle of boost converter

 η = efficiency of the converter, e.g. estimated 90%

The inductor current ripple determines maximum output current according to device switching current limit value.

$$I_{OUT_max} = I_{L_max} \times (1 - D) = \left(I_{SW_LIM} - \frac{\Delta I_L}{2}\right) \times (1 - D)$$

Where:

 I_{SW_LIM} = current limit of STI3508CB, it is 2.5A typically.

Generally, the inductor current ripple is estimated 10% to 40% of the input inductor average current. The recommended value of inductor is 2.2µH to 10µH for most application. The inductor should have low core loss at 1.2MHz and low DCR for better efficiency. To avoid inductor saturation current rating should be considered according to the switching current limit of STI3508CB.

Capacitor Selection

Input and output ceramic capacitors of 22µF are recommended for STI3508CB applications. As to output capacitor, the voltage rating is important for high voltage application and ceramic capacitor's derating under DC bias. Ceramic capacitors could reduce more than 50% its capacitance at its rated voltage. For better voltage filtering, ceramic capacitors with low ESR are recommended. X5R and X7R types are suitable because of their wider voltage and temperature ranges.

Table 1: Recommended Selection for Common Output Voltages of STI3508CB

V _{OUT} (V)	R1 (kΩ)	R2 (kΩ)	C _{оυт} (µF)	L (µH)
5	110	35.7	22	2.2 - 6.8
9	130	20.5	22	4.7 - 10
12	130	14.7	22	10
24	200	10.7	22	10



Diode Selection

Schottky diode is a good choice for STI3508CB because of its low forward voltage drop and fast reverses recovery. Using Schottky diode can get better efficiency. The high-speed rectification is also a good characteristic of Schottky diode for high switching frequency. Current rating of the diode must meet the root mean square of the peak current and output average current multiplication as following:

 $I_D(RMS) \approx \sqrt{I_{OUT} \times I_{PEAK}}$

The diode's reverse breakdown voltage should be larger than the output voltage.

Layout Consideration

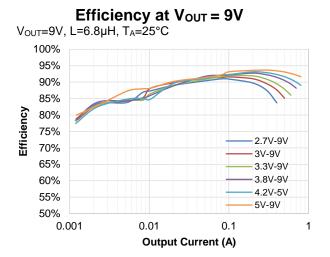
For best performance of the STI3508CB, the following guidelines must be strictly followed:

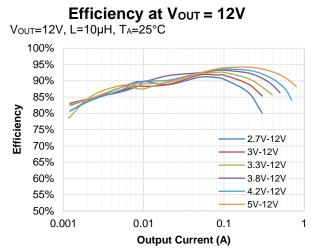
- Input and Output capacitors should be placed close to the IC and connected to ground plane
 to reduce noise coupling. Input Capacitor should be placed close to IN pin and GND. Output
 capacitor must be placed close to diode output node and GND to reduce switching loop.
- 2. Keep the main current traces as possible as short and wide.
- 3. LX node of DC-DC converter is with high frequency voltage swing. It should be kept at a small area.
- 4. Place the feedback divider resistor R1 and R2 as close as possible to the IC FB pin and keep away from the noisy devices since FB is a high impedance input pin that is sensitive to noise.

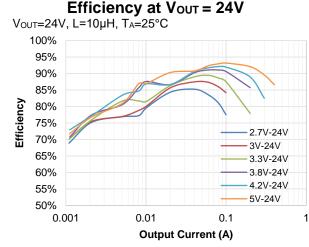


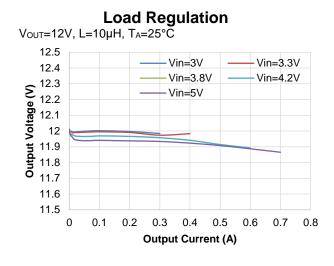
TYPICAL PERFORMANCE CHARACTERISTICS

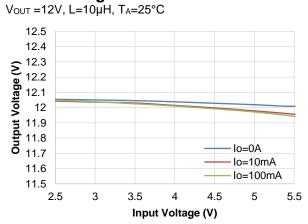
Efficiency at Vout = 5V V_{OUT}=5V, L=4.7μH, T_A=25°C 100% 95% 90% 85% Efficiency 80% 75% 2.7V-5V 70% 3V-5V 65% 3.3V-5V 60% 3.8V-5V 55% 4.2V-5V 50% 0.001 0.01 0.1 10 **Output Current (A)**







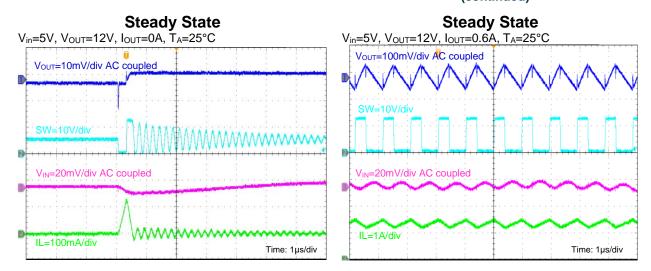


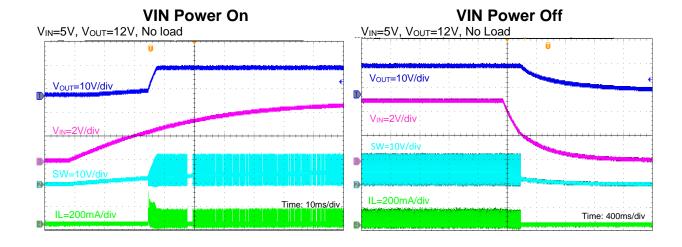


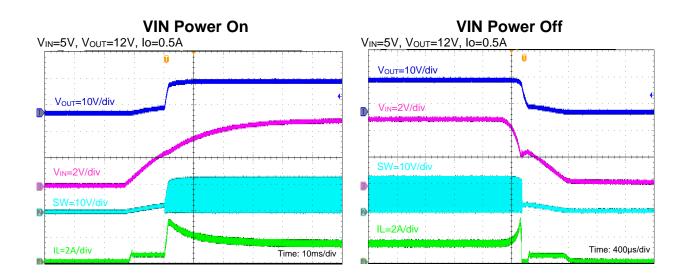
Line Regulation at Vout = 12V



TYPICAL PERFORMANCE CHARACTERISTICS (continued)



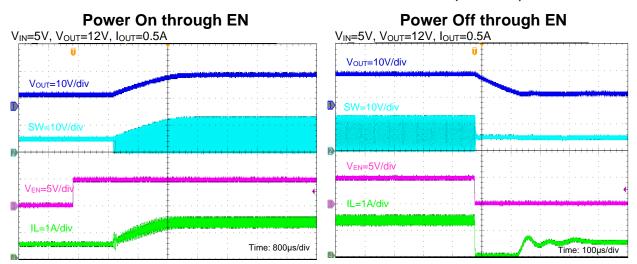


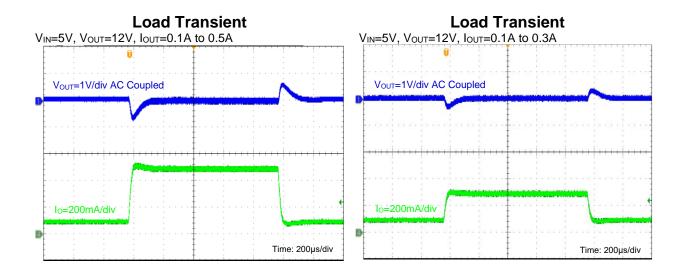


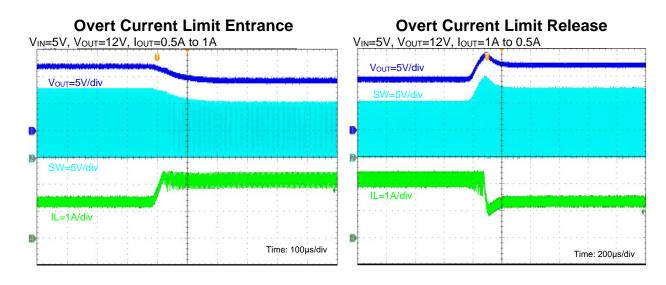
STI3508CB V1.0 2020.04



TYPICAL PERFORMANCE CHARACTERISTICS (continued)



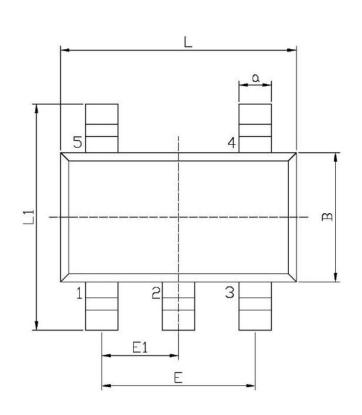


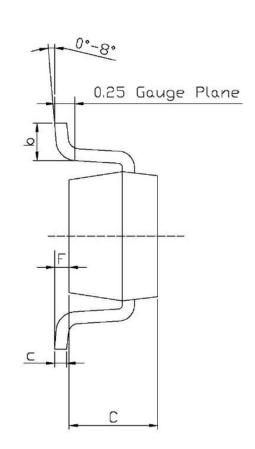




PACKAGE INFORMATION

SOT23-5





Unit: mm

Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters			
	Min	Тур	Max	Symbol	Min	Тур	Max
L	2.82	2.92	3.02	E1	0.85	0.95	1.05
В	1.50	1.60	1.70	а	0.35	0.425	0.50
С	0.90	1.10	1.30	С	0.10	0.15	0.20
L1	2.60	2.80	3.00	b	0.35	0.45	0.55
E	1.80	1.90	2.00	F	0	0.075	0.15

Note:

- 1) All dimensions are in millimeters.
- 2) Package length does not include mold flash, protrusion or gate burr.
- 3) Package width does not include inter lead flash or protrusion.
- 4) Lead popularity (bottom of leads after forming) shall be 0.10 millimeters max.
- 5) Pin 1 is lower left pin when reading top mark from left to right.

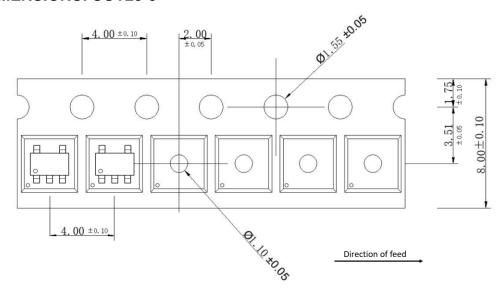


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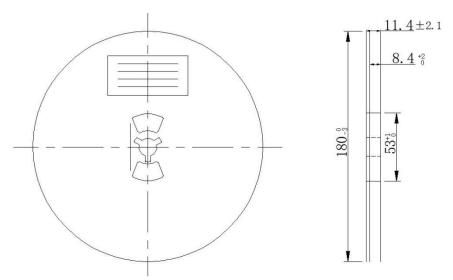


TAPE AND REEL INFORMATION

TAPE DIMENSIONS: SOT23-5



REEL DIMENSIONS: SOT23-5



Note:

- 1) All Dimensions are in Millimeter
- 2) Quantity of Units per Reel is 3000
- 3) MSL level is level 3.