



ZXCT180

26V, 400kHz BANDWIDTH, UNI-DIRECTIONAL LOW-SIDE AND HIGH-SIDE CURRENT MONITOR

Description

The ZXCT180 is a uni-directional current shunt monitor which is designed to be a cost-effective solution to deal with 26V common-mode in a smaller supply voltage range 2.7V to 5.5V powering the device.

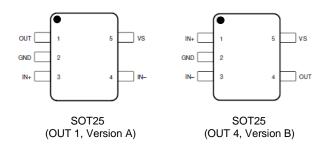
The ZXCT180 supports the small-signal bandwidths as high as 400kHz at 20V/V gain and fast large-signal slew rates of 2V/ μ s. This ability can be used to detect rapid changes in the sensed current and fast response to the output so that these features make the ZXCT180 a good choice for applications such as low-side motor control and the system fault current detection.

OUT pin is a voltage proportional to the load current. Based on OUT pin configurations, ZXCT180 has Pinout A and B as a flexibility for users to choose a proper one for PCB layout. In addition, this device has four fixed voltage gain options in 20V/V, 50V/V, 100V/V, and 200V/V. It can measure voltage across shunts at common-mode voltages from -0.3V to 26V, independent of supply voltage with a maximum supply current of 370µA. All versions are AEC-Q100 grade 1 temperature range, -40°C to +125°C, and are offered in the SOT25 package.

Features

- Supply Voltage Range: 2.7V to 5.5V
- Wide Common-Mode Range: -0.3V to 26V
- Gain Error: (Maximum over Temperature): ±1%
- · Choice of Gains:
 - Pinout A1 and B1: 20V/V
 - Pinout A2 and B2: 50V/V
 - Pinout A3 and B3: 100V/V
 - Pinout A4 and B4: 200V/V
- Low Offset Voltage: Typical ±100μV and 1-μV/°C Drift
- Rail-to-Rail Output Capability
- Low Quiescent Current: 370µA (max)
- Package: 5-Pin SOT25
- ESD Protection JESD 22, 5000V HBM (A114)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Pin Assignments



Applications

- Motor controls
- Lighting controls
- Power management
- Fault current detection
- Current sensing (high-side/low-side)
- Solar inverters

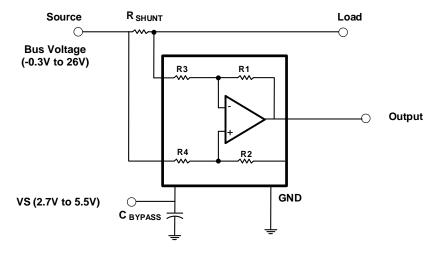
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Typical Applications Circuit

Device	OUT OUT	Cain Ontion	Gain	Gain Error	V _{OS (MAX)}	Gain Drift	
Device	(Pin 1)	(Pin 4)	Gain Option	V/V	%	μV	μV/°C
	А	В	1	20	1.0	±400	1
ZXCT180	Α	В	2	50	1.0	±400	1
	Α	В	3	100	1.0	±400	1
	А	В	4	200	1.0	±400	1



Recommendation of C_{BYPASS} 0.1 μF

 $V_{OUT} = (I_{LOAD} \times R_{SHUNT}) \times GAIN$

Pin Descriptions

Dis Noss	Pin Ni	umber	1/0	Description	
Pin Name	Pinout A	Pinout B	I/O		
GND	2	2	_	Ground	
VS	5	5	Power	Power supply, 2.7V to 5.5V	
IN+	3	1	Analog input	Connect to supply side of shunt resistor.	
IN-	4	3	Analog input	Connect to load side of shunt resistor.	
OUT	1	4	Analog output	Output voltage	



Absolute Maximum Ratings (@ TA = +25°C, unless otherwise specified.) (Note 4)

Parameter		Rating	Unit
Supply Volatge (Vs)		+6	V
A	Differential (IN+)-(IN-)	-28 to 28	V
Analog Inputs IN+, IN-	Common Mode	GND -0.3 to 26	V
REF Pin Input Voltage		GND- 0.3 to (Vs) + 0.3	V
OUT Pin Output Voltage		GND- 0.3 to (V _S) + 0.3	V
Max. Output Current (Note 5)		8	mA
ESD Human Body ESD Protection (HBM)		5	kV
ESD Charged Device Model ESD Protection (CDM)		1.5	kV
R _{0JA} (Junction-to-Ambient Thermal Resistance) (Note 6)		128	°C/W
R _{BJC} (Junction-to-Case Thermal Resistance) (Note 6)		37	°C/W
Operating Temperature, T _A		-40 to +125	°C
Storage Temperature		-65 to +150	°C

Notes:

- 4. Stresses greater than those listed under Absolute Maximum Ratings can cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to Absolute Maximum Ratings for extended periods can affect device reliability.
- 5. Input voltage at any pin can exceed the voltage shown if the current at that pin is limited to 5mA.
- 6. R_{BJA} and R_{BJC} are measured at T_A = +25°C on a high effective thermal conductivity minimum recommended pad (MRP) FR-4 PC board.

Recommended Operating Conditions (Note 7)

Symbol	Parameter	Min	Тур	Max	Unit
Vсм	Common-Mode Input Voltage	-0.3	12	26	V
Vs	Operating Supply Voltage (applied to VS)	2.7	5	5.5	V
TA	Operating Ambient Temperature	-40	_	+125	°C

Note: 7. Refer to the Typical Applications Circuit.



$\textbf{Electrical Characteristics} \ \, (T_A = +25 ^{\circ}\text{C}, \ V_S = 5 \text{V}, \ V_{IN+} = 12 \text{V}, \ V_{SENSE} = V_{IN+} - V_{IN-}, \ \text{and} \ V_{REF} = V_S \ / \ 2, \ unless \ otherwise \ noted.)$

Symbol	ı	Parameter	Test Conditions	Min	Тур	Max	Unit	
V _{CM}	Common-Mode Input		T _A = -40°C to +125°C	-0.3	_	26	V	
CMRR	Common-Mode Rejection		V _{IN+} = 0V to 26V, V _{SENSE} = 10mV	84	110	_	dB	
V	Official Value as DTI (Nata 0)		VSENSE = 0mV, VCM = 0V	_	±50	±150	μV	
Vos	Oliset voi	tage, RTI (Note 8)	VSENSE = 0mV, VCM = 12V	_	±100	±400	μV	
dV _{OS} /dT	Vos vs. T	emperature	$T_A = -40$ °C to +125°C	_	_	1.0	μV/°C	
_	Long-Terr	m Stability	Life test 1000 hours with T _A = +125°C (Note 9)	_	_	±400	μV	
PSRR	Power Su	pply Rejection	Vs = 2.7V to 5.5V V _{IN+} = 12V, V _{SENSE} = 10mV	_	±8	±40	μV/V	
lв	Input Bias	Current	VSENSE = 0mV, IN+ = 0V, IN- = 0V	_	-6	_	μΑ	
IB	приг ыаз	Current	VSENSE = 0mV, IN+ = 12V, IN- = 12V	_	75	_	μΑ	
los	Input Offs	et Current	V _{SENSE} = 0mV	_	±0.05	_	μΑ	
		ZXCT180A1 / B1	_	_	20	_		
G	Coin	ZXCT180A2 / B2	_	_	50	_	V/V	
G	Gain	ZXCT180A3 / B3	_	_	100	_	V/V	
		ZXCT180A4 / B4	_	_	200	_		
Eg	Gain Error		Vout = 0.5V to Supply Voltage – 0.5V T _A = -40°C to +125°C	_	±0.1%	±1%	_	
_	Gain Error vs. Temperature		$T_A = -40$ °C to +125°C	_	1.5	20	ppm/°C	
_	Nonlinearity Error		Vout = 0.5V to Supply Voltage - 0.5V	_	±0.01%	_	_	
Vон	Swing to 'Rail	/S Power-Supply	R_L = 10k Ω to GND, T_A = -40°C to +125°C	_	(Vs) – 0.02	(Vs) – 0.03	V	
Vol	Swing to	GND	R_L = 10k Ω to GND, T_A = -40°C to +125°C	_	(V _{GND}) + 0.0005	(VGND) + 0.005	V	
_	Maximum	Capacitive Load	No sustained oscillation	_	1	_	nF	
			CLOAD = 10pF, ZXCT180A1 / B1	_	400	_		
GBW	Bandwidtl		CLOAD = 10pF, ZXCT180A2 / B2	_	300	_	k∐z	
GBW	Dariuwiuti	1	C _{LOAD} = 10pF, ZXCT180A3 / B3	_	150	_	kHz	
			CLOAD = 10pF, ZXCT180A4 / B4	_	85	_		
SR	Slew Rate		_	_	2	_	V/µs	
_	Voltage Noise Density		_	_	40	_	nV/√Hz	
Vs	Operating Voltage Range		T _A = -40°C to +125°C	2.7	_	5.5	V	
,	Outessa	t Current	Vsense = 10mV	_	250	355	μA	
lq	Quiescen	Current	Vsense = 10mV, T _A = -40°C to +125°C	_	_	370	μA	
_	Specified	Range	_	-40	_	+125	°C	
_	Operating	Range	_	-40	_	+125	°C	

Notes:

^{8.} RTI = Referred to input

^{9.} For the long-term stability, the maximum V_{OS} shift with time has been calculated during life test 1000 hours with T_A = +125°C.

This V_{OS} drift with time is not a linear function of time, and the shift is great initially and slows down (becomes linear) over time. This parameter is guaranteed by design.



Typical Performance Characteristics

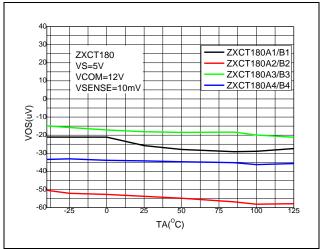


Figure 1. Offset Voltage vs. Temperature

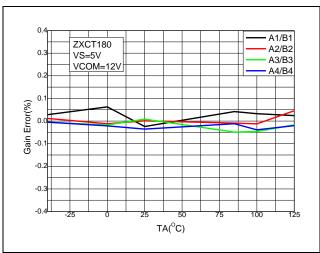


Figure 2. Gain Error vs. Temperature

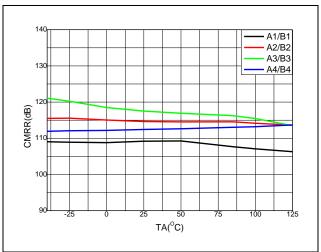


Figure 3. CMRR vs. Temperature

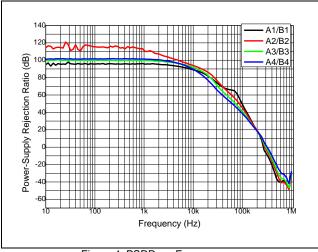


Figure 4. PSRR vs. Frequency

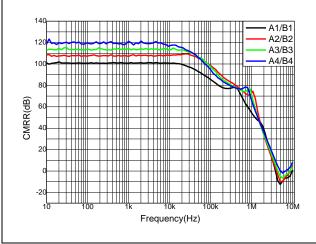


Figure 5. CMRR vs. Frequency

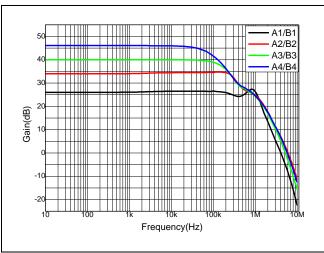


Figure 6. Gain vs. Frequency



Typical Performance Characteristics (continued)

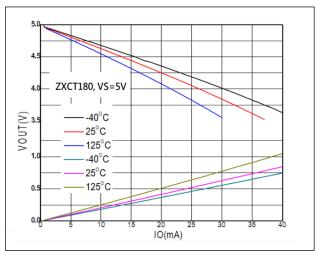


Figure 7. Output Voltage Swing vs. Output Current

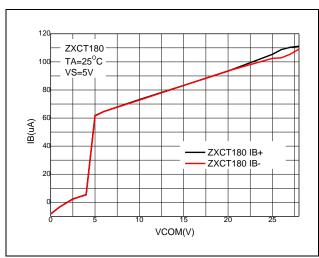


Figure 8. Input Bias Current vs. Common-Mode Voltage

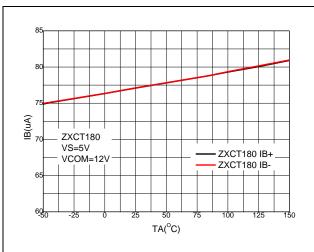


Figure 9. Input Bias Current vs. Temperature

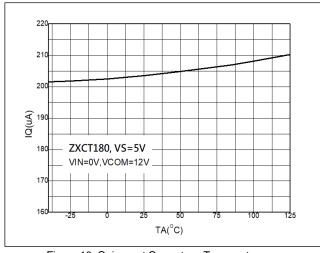


Figure 10. Quiescent Current vs. Temperature

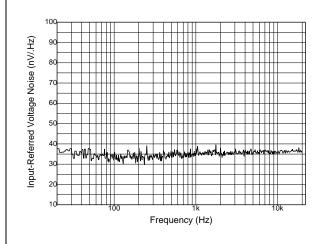


Figure 11. Input Noise Voltage vs. Frequency

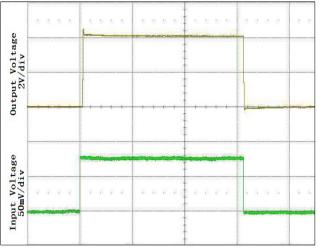


Figure 12. Step Response for A2 (G=50), Input 80mV vs. Output 2V



Application Information

General Information

The ZXCT180 has a wide common mode of -0.3V to 26V and it can be used in low-side and high-side current sensing. The device is a specially designed, current-sensing amplifier able to accurately measure voltages developed across a current-sensing resistor on common-mode voltages that far exceed the supply voltage powering the device. The chip topology enables high-precision measurements with maximum input offset voltages as low as $400\mu\text{V}$, with a maximum temperature contribution of $1.0\mu\text{V}/^{\circ}\text{C}$ over the full temperature range of -40°C to $+125^{\circ}\text{C}$.

Input and Output Pin Voltage Ranges

The ZXCT180 can withstand the full input signal range of up to 26V at the input sense pins, regardless of whether the device has power applied or has the VS pin at zero volts. The input circuitry of the ZXCT180 can accurately measure beyond its power-supply voltage. For example, the VS power supply is 5V, whereas the voltage applied to the analog input pins IN+ or IN- can be as high as 26V. At the moment, the output voltage range of the OUT pin is limited by the voltage on the power supply and a consideration of the maximum VoH.

Power Supply Recommendations

The ZXCT180 can operate on as low as 2.7V. In some applications the power supply may be selected to limit the output range that is compatible with parts using this signal such as an analog converter or an analog input pin on a microcontroller. The power supply has been set at 5V. This will limit the output voltage of the current monitor. The ground connections for the ADC and current monitor should be wired as a star configuration to minimize error. A power-supply bypass capacitor connected closely to the device pins is required for stability with a suggested value of 0.1μ F. Please place the power-supply bypass capacitor as close as possible to the device power supply and ground pins. Additional decoupling capacitance can be added to compensate for noisy or high-impedance power supplies.

Figure 13 shows the basic connections of the ZXCT180. Connect the input pins (IN+ and IN-) as closely as possible to the shunt resistor to minimize any resistance in series with the shunt resistor.

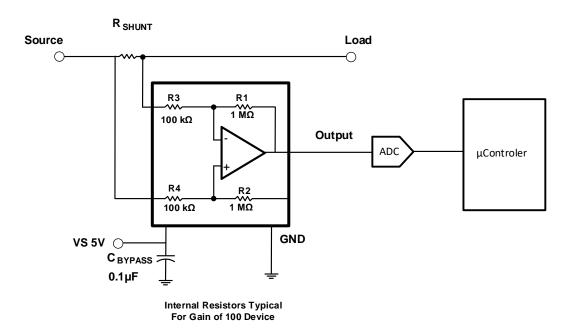
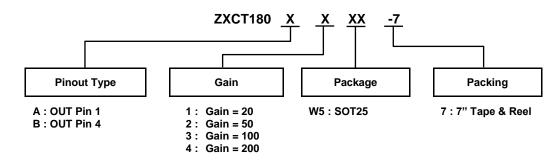


Figure 13. Typical Application



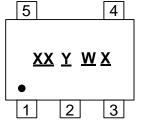
Ordering Information



Orderable Part Number	Outrot	Output Package Code	Package	Packing		
Orderable Part Number	Output			Qty.	Carrier	
ZXCT180A1W5-7	Pin 1	W5	SOT25	3000	7" Tape & Reel	
ZXCT180A2W5-7	Pin 1	W5	SOT25	3000	7" Tape & Reel	
ZXCT180A3W5-7	Pin 1	W5	SOT25	3000	7" Tape & Reel	
ZXCT180A4W5-7	Pin 1	W5	SOT25	3000	7" Tape & Reel	
ZXCT180B1W5-7	Pin 4	W5	SOT25	3000	7" Tape & Reel	
ZXCT180B2W5-7	Pin 4	W5	SOT25	3000	7" Tape & Reel	
ZXCT180B3W5-7	Pin 4	W5	SOT25	3000	7" Tape & Reel	
ZXCT180B4W5-7	Pin 4	W5	SOT25	3000	7" Tape & Reel	

Marking Information

(Top View)



 \underline{XX} : Identification Code

 \underline{Y} : Year 0 to 9 (ex: 4 = 2024) \underline{W} : Week: A to Z: week 1 to 26;

a to z: week 27 to 52; z represents

week 52 and 53

X: Internal Code

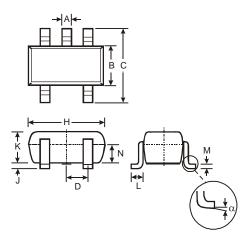
Orderable Part Number	Package	Identification Code
ZXCT180A1W5-7	SOT25	MA
ZXCT180A2W5-7	SOT25	MB
ZXCT180A3W5-7	SOT25	MC
ZXCT180A4W5-7	SOT25	MD
ZXCT180B1W5-7	SOT25	ME
ZXCT180B2W5-7	SOT25	MF
ZXCT180B3W5-7	SOT25	MG
ZXCT180B4W5-7	SOT25	MH



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT25

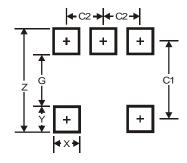


	SOT25			
Dim	Min	Max	Тур	
Α	0.35	0.50	0.38	
В	1.50	1.70	1.60	
С	2.70	3.00	2.80	
D	-	ı	0.95	
Н	2.90	3.10	3.00	
J	0.013	0.10	0.05	
K	1.00	1.30	1.10	
L	0.35	0.55	0.40	
М	0.10	0.20	0.15	
N	0.70	0.80	0.75	
α	0°	8°	-	
AII D	All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT25



Dimensions	Value
Z	3.20
G	1.60
Х	0.55
Υ	0.80
C1	2.40
C2	0.95

Mechanical Data

SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.012 grams (Approximate)
- Max Soldering Temperature +260°C for 30 secs as per JEDEC J-STD-020



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