

# 6 String White LED Driver with Boost Converter

#### DESCRIPTION

The EUP2990A is a high efficiency boost regulator with 6 channel current sinker which is ideal for media size LCD backlighting applications.

The device with integrated 2.5A/43V power MOSFET uses current mode architecture and drives six strings in parallel and supports up to 10 pieces of LEDs per string. The switching frequency is programmable by an external frequency setting resistor. The EUP2990A regulates the current in each LED string to the programmed value set by an external current setting resistor. The EUP2990A current matching can achieve ±2%. To provide enough headroom for the operating of current sink, boost converter monitors the minimum voltage of feedback pins and regulates an optimized output voltage for power efficiency.

The EUP2990A has wide input voltage range from 2.5V to 24V and provides adjustable 5mA to 50mA LED current. It provides cycle-by-cycle over current protection. The device supports PWM, analog and mix mode brightness dimming functions for accurate LED current control.

The EUP2990A integrated multiple protect functions, such as LED open, LED short, output over voltage, over thermal and under voltage lockout, these protections will prevent the LCD backlight from damage.

The EUP2990A is available in a 3×3mm TQFN-20L package.

#### **FEATURES**

- 2.5V to 24 V Input Voltage
- 6 Strings in Parallel and Up to 10 WLEDs Per String
- Integrated 2.5A 43V MOSFET
- Output Voltage Up to 43V
- Programmable Full Channel Current from 5mA to 50mA
- Direct PWM Dimming up to 25kHz
- PWM to Analog Dimming up to 20kHz with 8 bit resolution
- Better Than 2% Current Regulation Accuracy Between Strings
- Multiple Fault Protections
  - -Current Limit Protection
  - -Over Voltage Protection
  - -Over Temperature Protection
  - -WLED Open/Short Protection
- Available in 3mm×3mm TQFN-20 Package
- RoHS Compliant and 100% Lead (Pb)-Free Halogen-Free

#### **APPLICATIONS**

- Notebook LCD Display Backlight
- UMPC LCD Display Backlight

## **Typical Application Circuit**

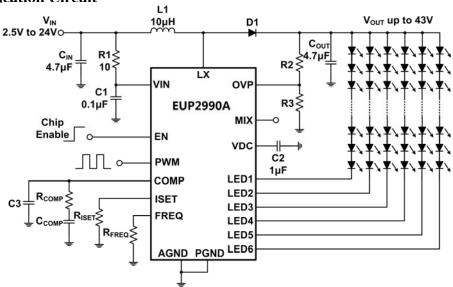


Figure 1.



**Pin Configurations** 

Package Type	Pin Configurations
TQFN-20	(TOP VIEW)    N

# **Pin Description**

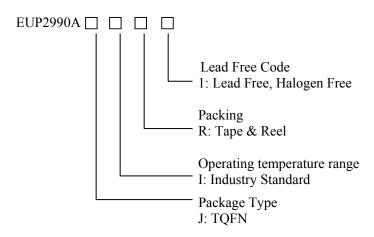
PIN	TQFN-20	DESCRIPTION
1	EN	Chip enable pin (active high). There is an internal pull low resistor connected to AGND.
2	FREQ	Switching frequency of Boost converter setting input. Connect a resistor to ground.
3	ISET	LED current is set by the value of the resistor $R_{ISET}$ Connected from the ISET pin to ground. $V_{ISET}$ is around 1V, during full lighting, $I_{LEDX}$ =240/ $R_{ISET}$ . Set $R_{ISET}$ =12K $\Omega$ , $I_{LEDX}$ is set to 20mA.
4	MIX	Dimming mode select pin. Floating for 25% Analog + PWM dimming, pulling to GND for pure dc dimming, pulling to VDC for direct PWM dimming.
5	AGND	Analog Ground of LED Driver.
6~11	LED6 ~ LED1	Current Sink for LED String. (Leave the pin unconnected or short to GND, if not used.)
12	OVP	Over voltage protection for Boost converter. Connect OVP to the tap point of the resistor divider between output and ground. The detecting threshold is 1.2V.
13,14	PGND	Power ground of Boost converter.
15,16	LX	The switching for Boost converter. Drain of the internal power NMOS.
17	PWM	PWM dimming control input.
18	VDC	Internal regulator voltage. Bypass VDC to ground with a ceramic capacitor.
19	VIN	Supply voltage input.
20	COMP	Boost converter compensation pin. Connect a compensation network to ground.
Thermal Pad	GND	The exposed pad must be soldered to a large PCB and connected to GND for maximum power dissipation.



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# **Ordering Information**

Order Number	Package Type	Marking	Quantity per Reel	Operating Temperature Range
EUP2990AJIR1	TQFN-20	XXXXX 2990A	2500	-40 °C to +85°C



# **Block Diagram**

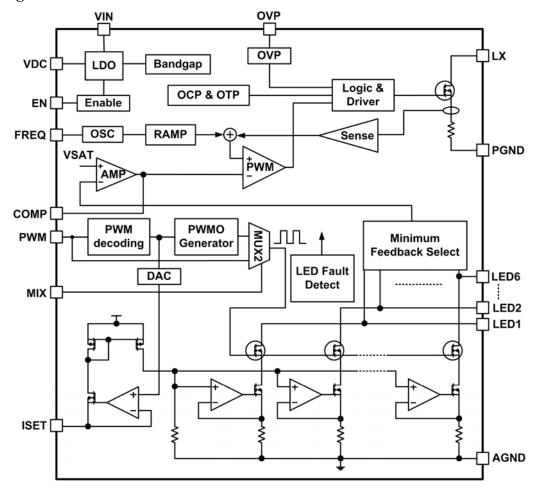


Figure 2.



# **Absolute Maximum Ratings (1)**

•	VIN to GND	-0.3V to $+30V$
•	EN, PWM, ISET, COMP, MIX, FREQ to GND	-0.3V to +30V
•	LX, OVP, LED1 to LED6 to GND	-0.3V to +45V
•	VDC to GND	-0.3V to $+6V$
•	PGND to GND	-0.3V to $+0.3V$
•	Continuous Power Dissipation (T <sub>A</sub> = +25°C) TQFN3×3-20	2.083W
•	Package Thermal Resistance, $\theta_{JA}$ (TQFN-20)	60°C/W
•	Junction Temperature	+150°C
•	Storage Temperature Range	-60°C to +150°C
•	Lead Temperature (Soldering, 10sec)	260°C
•	ESD Rating Human Body Model	2kV
iende	ed Operating Conditions (2)	

## Recomme

- Battery Input Voltage (VIN) -----2.5V to 24V
- Operating Temperature Range ------40°C to +85°C

Note (1): Stress beyond those listed under "Absolute Maximum Ratings" may damage the device.

Note (2): The device is not guaranteed to function outside the recommended operating conditions

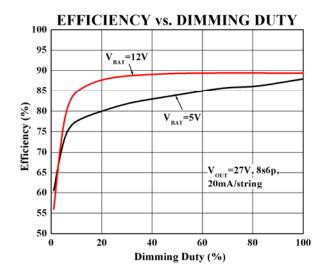
## **Electrical Characteristics**

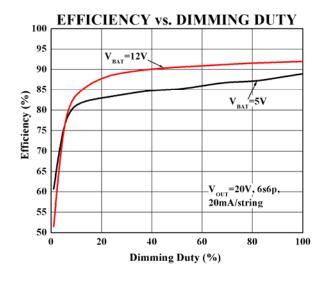
 $(V_{IN}=5V, R_{ISET}=12K\Omega, T_A=-40^{\circ}C \text{ to } 85^{\circ}C, \text{ unless otherwise specified. Typical values are at } T_A=+25^{\circ}C.)$ 

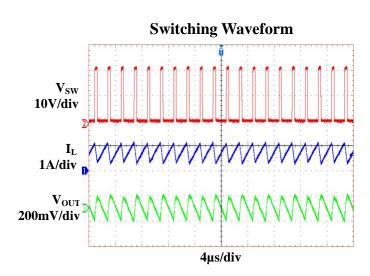
Crymbol	Parameter	Conditions	EUP2990A			Unit	
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Omt	
V <sub>IN</sub>	Input voltage		2.5		24	V	
I <sub>VIN_NOSW</sub>	VIN Quiescent Current	V <sub>PWM</sub> =V <sub>COMP</sub> =0V, No Switching		3		mA	
$I_{VIN\_SW}$	VIII Quiescent Current	V <sub>PWM</sub> =V <sub>COMP</sub> =2V, Switching	4			IIIA	
$I_{SHDN}$	VIN Shutdown Current	$V_{IN}$ =4.5V, $V_{EN}$ =0V			10	μA	
$V_{\rm IN\_UVH}$	VIN Under Voltage Lockout	V <sub>IN</sub> Rising		2.3		V	
$V_{\rm IN\_UVL}$	Threshold	V <sub>IN</sub> Falling		2.1		V	
$V_{\mathrm{IH}}$	EN, PWM, MIX Input	$V_{IN}$ =2.5V to 24V	1.8			V	
$V_{ m IL}$	Voltage	$V_{IN}$ =2.5V to 24V			0.8	v	
<b>Boost Conv</b>	erter						
$F_{SW}$	Switching Frequency	$R_{FSW}=51K\Omega$		500		KHz	
$D_{MAX}$	Maximum Duty Cycle	$F_{SW}=1MHz$		92		%	
T <sub>ON_MIN</sub>	Minimum On Time	$F_{SW}=1MHz$		80		ns	
R <sub>ON_LX</sub>	LX On Resistance			0.2		Ω	
$I_{LIM}$	Boost Current Limit			2.5		A	
LED Curre	nt Regulation						
$V_{ m LEDX}$	Regulated V <sub>LEDX</sub>	I <sub>LED</sub> =20mA		0.5		V	
$I_{LEDA}$	Output LED Current	$R_{ISET}=12K\Omega$	19.2	20	20.8	mA	
$I_{LEDM}$	LED Current Matching	$R_{ISET}=12K\Omega$	-2		2	%	
$V_{ISET}$	ISET Regulation Voltage			1		V	
<b>Fault Prote</b>	ction						
$V_{OVP}$	OVP Threshold	Rising Edge		1.2		V	
$V_{OVPS}$	OVP Short Threshold			50		mV	
$T_{SD}$	Thermal Shutdown Threshold			150		°C	
$T_{SD\_HYS}$	Thermal Shutdown Hysteresis			15		°C	
$V_{\text{LED\_UV}}$	LED Pin Under Voltage Threshold			0.17		V	

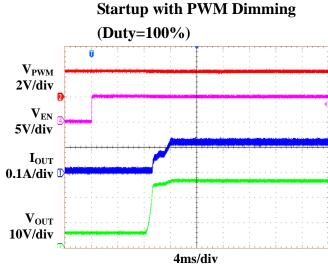


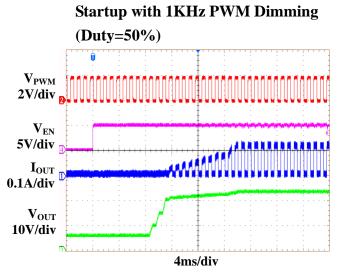
# **Typical Operating Characteristics**

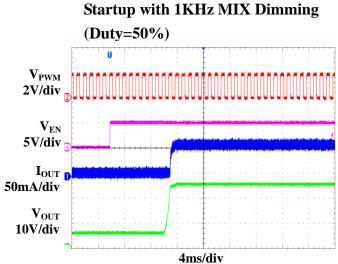






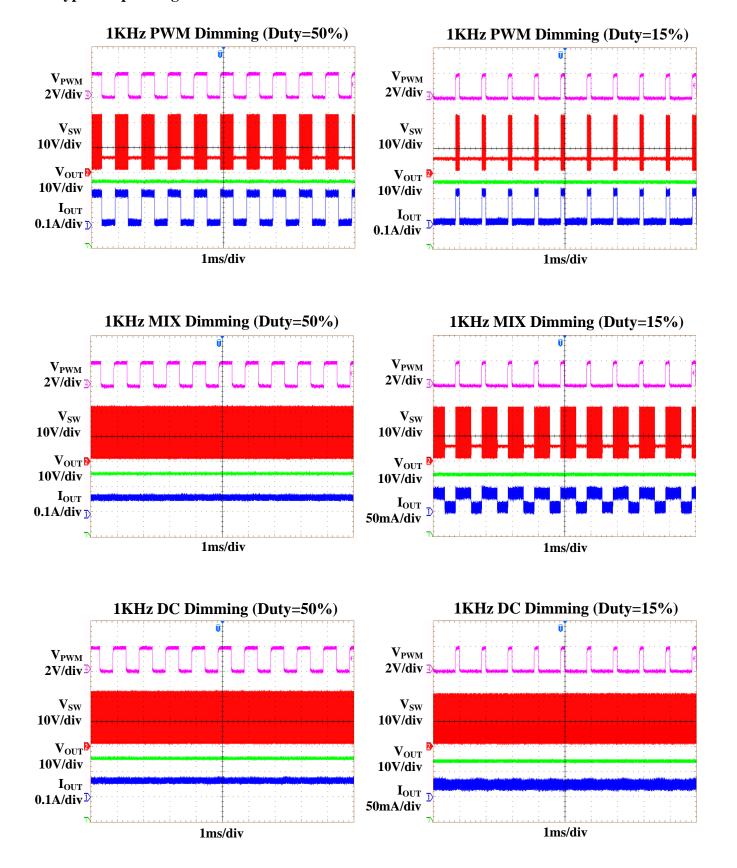






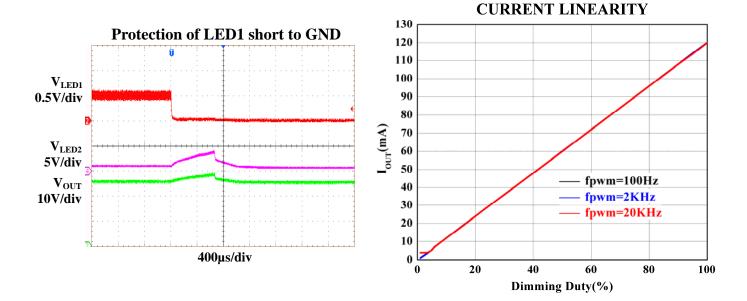
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# **Typical Operating Characteristics (Continued)**





# **Typical Operating Characteristics (Continued)**





## **Detailed Descriptions**

The EUP2990A is a high-efficiency driver for arrays of white LEDs. It contains a current mode, PWM step-up converter with 2.5A/43V power MOSFET, linear regulator, dimming control circuit, and 6 channel regulated current sources. Each channel can provide adjustable 5mA to 50mA LED current. When enabled, the step-up converter boosts the output voltage to provide sufficient headroom for the current sources to regulate their respective string currents. The control architecture automatically skips pulses at light loads to improve efficiency and prevents overcharging the output capacitor. The EUP2990A integrates PWM dimming, mixed mode dimming and pure dc dimming function for accurate LED current control. It also has multiple features to protect the converter from fault conditions. Separate feedback loops limit the output voltage if one or more LEDs fail open or short. The converter features cycle-by-cycle current limit to provide consistent operation. A thermal-shutdown circuit provides another level of protection.

#### **System Startup**

A logic high signal on the EN pin turns on the internal linear regulator which provides supply to the IC current. The soft start time of the LED boost converter is several tens of milliseconds according to the compensation capacitor on the COMP pin.

#### **Step-up Converter**

The Step-up regulator is a high efficiency current mode PWM architecture. The converter automatically chooses the lowest active LEDX voltage to regulate the output voltage.

As the load changes, the error amplifier sources or sinks current to the COMP output to deliver the required peak-inductor current. The slope-compensation signal is added to the current-sense signal to improve stability at high duty cycles.

At light-load or  $V_{OUT}$  near to  $V_{IN}$  operation, the converter runs into the pulse-skipping mode, the FET is turned on for a minimum on-time of approximately 80ns, and then the converter discharges the power to the output in the remain period. The MOSFET will keep off until the output voltage needs to be boosted again

# **Setting and Regulation of LED Current**

The six current sink regulators embedded in EUP2990A can output from 5mA to 50mA current each. The full-scale current per channel is programmed by the resistor at ISET pin according to the equation:

 $I_{LED}(mA)=240/R_{ISET}(K\Omega)$ 

Where  $R_{\text{ISET}}$  is the resistor between the ISET pin and GND

The DC/DC converter regulates the LED current according to the setting.

### **Dimming Control**

The EUP2990A provides three ways of controlling the LED brightness: Direct PWM dimming mode, mixed dimming mode and pure DC dimming mode.

### 1. Direct PWM Dimming

When bias the MIX pin to VDC, the device operates in Direct PWM dimming mode. An external PWM dimming signal is employed to achieve PWM dimming control. The current source turn-on/off is synchronized with the PWM signal. The brightness of the LED array is proportional to the duty cycle of the PWM signal.

#### 2. Mixed-Mode Dimming

If the MIX pin is floating, the device operates in Mixed Mode. In this mode an internal PWM decoding circuit detects the on time and the period of the PWM signal and calculates the duty cycle information. Then the IC controls the LED current according to the duty cycle to realize the brightness dimming.

When  $25\% \leq PWM$  duty  $\leq 100\%$ , analog dimming mode is auto-implemented. The brightness dimming is realized by controlling the DC current of WLED diodes. Since the forward voltage of a WLED diode drops when its DC current reduces, the required output voltage can become lower when dimming duty cycle goes low, reducing the power budget for the backlight. When PWM duty  $\leq 25\%$ , PWM dimming mode is auto-implemented, eliminating potential color shift effect which normally happens when the DC current of WLED diode goes low. The LED current is fixed at  $0.25\times I_{LED\_FULL}$ , and the dimming duty is  $4\times PWM$  duty. A 8-bit D/A resolution is implemented for the LED current regulation.

#### 3. Pure DC Dimming

When bias the MIX pin to GND, the device operates in pure dc dimming mode. The brightness dimming is realized by controlling the DC current of WLED diodes. Also an 8-bit D/A resolution is implemented for the LED current regulation.

#### The Range of Dimming Frequency and Duty

A digital PWM dimming signal adjusted from 100Hz to 20KHz can be applied to the PWM pin. Table 1 and Table 2 show the range of dimming frequency, minimum duty and maximum duty of the PWM signal for three ways of dimming control.



Table 1. PWM and Mixed Dimming Mode

Dimming frequency	Duty (min.)	Duty (max.)			
20kHz	3.5%	100%			
19kHz	3.4%	100%			
18kHz	3.2%	100%			
17kHz	3%	100%			
16kHz	2.9%	100%			
15kHz	2.7%	100%			
14kHz	2.5%	100%			
13kHz	2.3%	100%			
12kHz	2.2%	100%			
11kHz	2%	100%			
10kHz	1.8%	100%			
9kHz	1.6%	100%			
8kHz	1.4%	100%			
7kHz	1.3%	100%			
6kHz	1.1%	100%			
100Hz~5kHz	1%	100%			

Table 2. Pure DC Dimming Mode

Dimming frequency	Duty (min.)	Duty (max.)
100Hz~20kHz	4%	100%

The dimming frequency of the direct PWM dimming can be up to 25 kHz. Then the minimum duty of dimming signal increases to 5%.

#### **Over-Current and Over-Voltage Protection**

The EUP2990A has pulse-by-pulse over-current limit of 2.5A (typ.). The PWM switch turns off when the inductor current reaches this current threshold. The PWM switch remains off until the beginning of the next switching cycle. This protects the IC and external components under over-load conditions.

The output voltage of the boost converter is detected by OVP pin. When the voltage at the OVP pin reaches a threshold of approximately 1.2V, the driver will turn off. The driver will turn on again when the voltage at OVP drops a certain voltage below 1.2V. The output OVP threshold can be programmed by an external resistor divider and calculated by the following equation:

$$V_{OUT\_OVP} = 1.2 \times \left(\frac{R2 + R3}{R3}\right)$$

### **LED Open String Protection**

If one of the WLED strings is open, the IC keeps charging the output voltage until it reaches OVP threshold. Then the part will mark-off the open string. As a result, the IC removes the open LED pin from the voltage feedback loop. Subsequently, the output voltage drops down and is regulated to a voltage for the connected WLED strings. The LED current of the connected WLED strings keep in regulation during the whole transition. The EUP2990A always tries to light at least one string and if all strings in use are open, the EUP2990A shuts down the step-up converter.

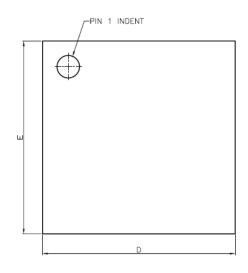
#### **LED Pin Unused**

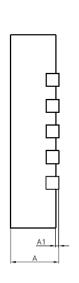
If the application requires less than 6 WLED strings, one can easily disable unused LED pins. The EUP2990A simply requires leaving the unused LED pin open or shorting it to ground. If the LED pin is open, the boost output voltage ramps up to V<sub>OUT</sub> over voltage threshold during start up. The IC then detects the zero current string, and removes it from the feedback loop. If the LED pin is shorted to ground, the IC detects the short immediately after IC enable, and the boost output voltage does not go up to V<sub>OUT</sub> over voltage threshold. Instead, it ramps to the regulation voltage after soft start.

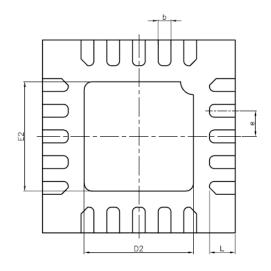


# **Packaging Information**

TQFN-20







Note: Exposed pad outline drawing is for reference only.

SYMBOLS	MILLIMETERS		INCHES			
	MIN.	Normal	MAX.	MIN.	Normal	MAX.
A	0.70	0.75	0.80	0.028	0.030	0.031
A1	0.00	-	0.05	0.000	-	0.002
b	0.13	0.20	0.27	0.005	0.008	0.011
Е	2.90	3.00	3.10	0.114	0.118	0.122
D	2.90	3.00	3.10	0.114	0.118	0.122
D2	1.45	1.70	1.75	0.057	0.067	0.069
E2	1.45	1.70	1.75	0.057	0.067	0.069
e	0.40 REF				0.016 REF	
L	0.30	0.40	0.50	0.012	0.016	0.020

