

DIO5518B

500 mA, Single Li-ion Battery Charger

Features

- Overtemperature protection
- Undervoltage lockout protection
- Overvoltage lockout protection
- Reverse current protection between BAT and GND pins
- Automatic recharge threshold 4.05 V (Typ.)
- Charge status output pin
- 2.9 V trickle charge threshold
- Soft-start limits inrush current

Descriptions

The DIO5518B is a complete constant-current / constant voltage linear charger for single cell Lithium-Ion batteries. No external sense resistor is needed, and no blocking diode is required due to the internal MOSFET architecture. Thermal feedback regulates the charge current to limit the die temperature during high power operation or high ambient temperature. The charge voltage is fixed at 4.2 V, and the charge current can be programmed externally with a single resistor.

The DIO5518B automatically terminates the charge cycle when the charge current drops to 1/10 the programmed value after the final float voltage is reached.

When the input supply (wall adapter or USB supply) is removed, the DIO5518B automatically enters a low current state, dropping the battery drain current to less than 0.5 μ A. The DIO5518B can be put into shutdown mode, reducing supply current to 40 μ A (Typ.).

The DIO5518B is available in a small package with SOT23-5. Standard product is Pb-Free.

Applications

- Wireless phone
- MP3/MP4 player
- Bluetooth device

Ordering Information

Ordering Part No.	Top Marking	MSL	RoHS	T _A	Package	
DIO5518BST5	W518	3	Green	-40 to 85°C	SOT23-5	Tape & Reel, 3000

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Pin Assignment

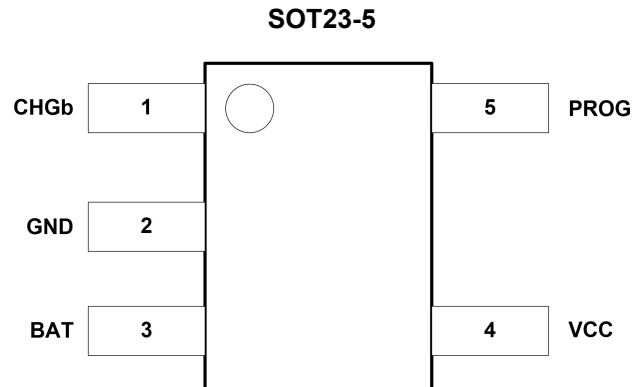


Figure 1. Top view

Pin Descriptions

Name	Description
CHGb	Open-drain charge status output. When the battery is charging, the CHGb pin is pulled low. When the charge cycle is completed or V_{CC} is removed, the CHGb is forced high impedance.
GND	Ground.
BAT	Charge current output. Provides charge current to the battery and regulates the final float voltage to 4.2 V.
VCC	Power supply.
PROG	Charge current setting, charge current monitor and shutdown pin. The charging current is given by $I_{BAT} = (1/R_{PROG}) * 1000$. The chip will be shutdown when PROG pin floating.

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Absolute Maximum Ratings

Stresses beyond those listed under “Absolute Maximum Rating” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other condition beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maxim rating conditions for extended periods may affect device reliability.

Symbol	Parameter	Rating	Unit
	Supply voltage	-0.3 to 10	V
	PROG voltage	-0.3 to V _{CC}	V
	BAT voltage	-0.3 to 10	V
	CHGb voltage	-0.3 to V _{CC}	V
	BAT pin current	500	mA
R _{θJA}	Thermal resistance, junction to ambient	250	°C/W
P _D	Power dissipation	0.6	W
T _J	Junction temperature	150	°C
T _{STG}	Storage temperature	-65 to 125	°C
T _L	Lead temperature (Soldering 10s)	260	°C

Recommend Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended Operating conditions are specified to ensure optimal performance to the datasheet specifications. DIOO does not Recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Rating	Unit
	Input supply voltage	4.5 to 5.5	V
T _A	Operating temperature range	-40 to 85	°C

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Electrical Characteristics

$V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$ (unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_{SOLYCHRG}	Charge mode supply current	$R_{\text{PROG}} = 2\text{ k}\Omega$		250	2000	μA
		$R_{\text{PROG}} = 20\text{ k}\Omega$		200	2000	μA
I_{BATCHRG}	Charge mode battery current	$R_{\text{PROG}} = 5\text{ k}\Omega$		200		mA
		$R_{\text{PROG}} = 2\text{ k}\Omega$	450	500	550	mA
		$R_{\text{PROG}} = 20\text{ k}\Omega$	44	49	54	mA
V_{PROGCHRG}	PROG pin voltage	$R_{\text{PROG}} = 2\text{ k}\Omega$	0.93	1.00	1.07	V
		$R_{\text{PROG}} = 20\text{ k}\Omega$	0.93	1.00	1.07	V
I_{SPLYSTBY}	Standby mode supply current	Charge terminated		130	500	μA
I_{BATSTBY}	Standby mode battery current	Charge terminated	0	-2.5	-6	μA
I_{SPLYASD}	Shutdown mode supply current	$V_{CC} < V_{\text{BAT}}$	20	42	90	μA
I_{BATASD}	Shutdown mode BAT pin current	$V_{CC} < V_{\text{BAT}}$		± 0.05	± 1	μA
I_{SPLYUVLO}	UVLO mode supply current	$V_{CC} < V_{\text{UV}}$	20	42	90	μA
I_{BATUVLO}	UVLO mode BAT pin current	$V_{CC} < V_{\text{UV}}$		± 0.05	± 1	μA
I_{SPLYOVLO}	OVLO mode supply current	$V_{CC} > V_{\text{OV}}$		40		μA
I_{BATOVLO}	OVLO mode BAT pin current	$V_{CC} > V_{\text{OV}}$		± 0.05	± 1	μA
I_{SPLYSHUT}	Shutdown mode supply current	R_{PROG} not connected	20	40	70	μA
I_{BATSHUT}	Shutdown mode BAT pin current	R_{PROG} not connected		± 0.05	± 1	μA
I_{BATMSD}	Manual shutdown BAT pin current	$V_{\text{PROG}} = 1.3\text{ V}$		± 0.05	± 1	μA
I_{BATSLEEP}	Sleep mode BAT pin current	$V_{CC} = 0\text{ V}$		± 0.05	± 1	μA
$I_{\text{Charge_terminated}}$	100 mA/10 mA charger terminated	$R_{\text{PROG}} = 10\text{ k}\Omega$		10		μA
$V_{\text{Charge_terminated}}$		$R_{\text{PROG}} = 10\text{ k}\Omega$		0.1		V
V_{FLOAT}	Float voltage		4.158	4.2	4.242	V
I_{TRIKL}	Trickle charge current	$R_{\text{PROG}} = 2\text{ k}\Omega$		50		mA
V_{TRIKL}	Trickle charge voltage threshold	$R_{\text{PROG}} = 2\text{ k}\Omega$	2.8	2.9	3.0	V
$V_{\text{TRIKL_HYS}}$	Trickle charge voltage hysteresis	$R_{\text{PROG}} = 2\text{ k}\Omega$		100		mV
V_{UVLO}	UVLO threshold	From V_{CC} low to high	3.6	3.8	4.0	V

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Electrical Characteristics (continued)

$V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$ (unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$V_{UVLO, HYS}$	UVLO hysteresis			250		mV
V_{OVLO}	OVLO threshold	From V_{CC} low to high		6		V
$V_{OVLO, HYS}$	OVLO hysteresis			180		mV
V_{MSD}	Manual shutdown threshold voltage	PROG pin rising		1.2	1.3	V
		PROG pin falling		1.0		
V_{ASD}	VCC -VBAT lockout threshold voltage	V_{CC} from low to high		120		mV
		V_{CC} from high to low	5	50		
$\Delta V_{RECHRG}^{(1)}$	Auto recharge battery voltage		100	150	200	mV
V_{CHGb}	CHGb pin output low voltage	$I_{CHGb} = 5\text{ mA}$		0.3	0.6	V
T_{LIM}	Junction temperature in CT mode			165		$^\circ\text{C}$
R_{ON}	Power FET ON resistance			250		m Ω
t_{SS}	Soft-start time	$R_{PROG} = 2\text{ k}\Omega$		50		μs
t_{RECHRG}	Recharge comparator filter time			2		ms
t_{TERM}	Termination comparator filter time			1		ms
$I_{PROG}^{(1)}$	PROG pin pull-up current		0.35	0.5	0.75	μA

Note:

- (1) Guaranteed by design.
- (2) Specifications subject to change without notice.

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Typical Performance Characteristics

$V_{CC} = 5\text{ V}$, $T_A = 25^\circ\text{C}$ (unless otherwise noted)

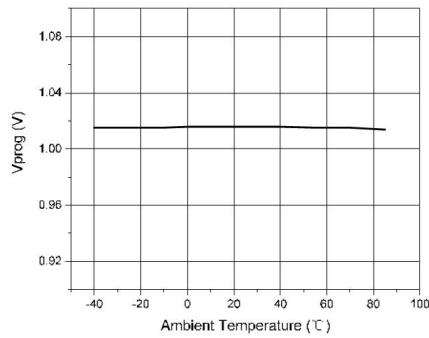


Figure 2. PROG pin voltage vs. Ambient temperature

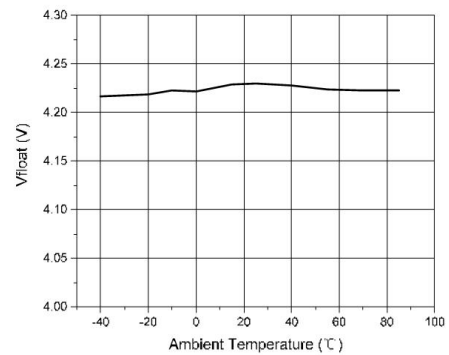
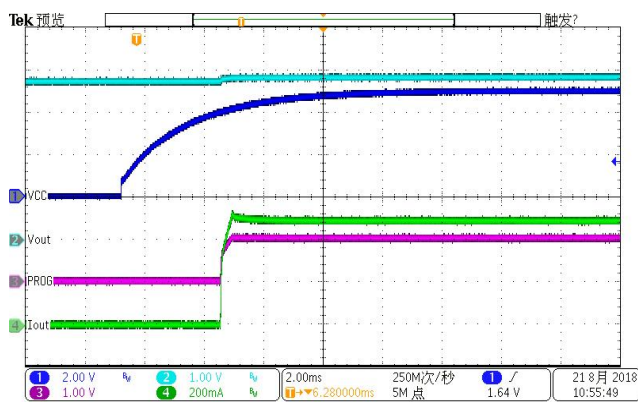
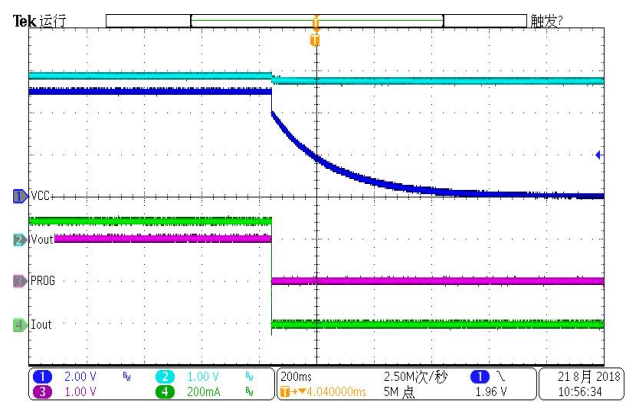


Figure 3. Float voltage vs. Ambient temperature



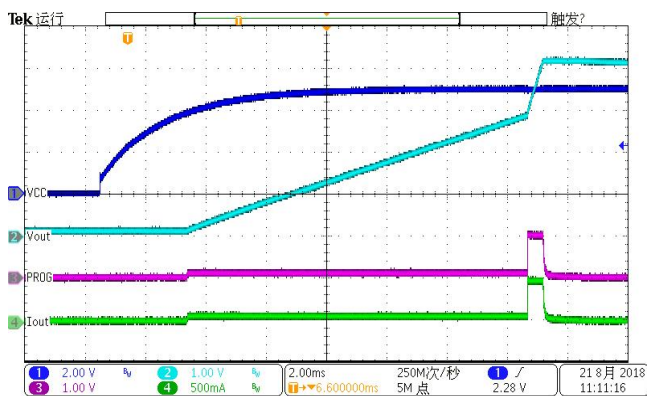
$V_{CC} = 5\text{ V}$, $R_{PROG} = 2\text{ k}\Omega$, $C_{BAT} = 10\text{ }\mu\text{F}$,
BAT connect to battery

Figure 4. VCC start-up



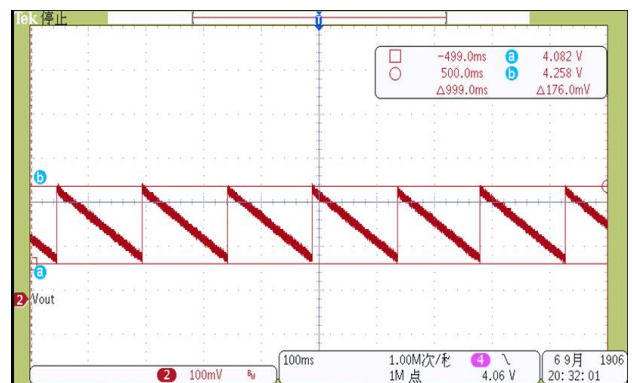
$V_{CC} = 5\text{ V}$, $R_{PROG} = 2\text{ k}\Omega$, $C_{BAT} = 10\text{ }\mu\text{F}$,
BAT connect to battery

Figure 5. VCC shut-down



$V_{CC} = 5\text{ V}$, $R_{PROG} = 2\text{ k}\Omega$, $C_{BAT} = 10\text{ }\mu\text{F}$

Figure 6. Charging curve



$V_{CC} = 5\text{ V}$, $R_{PROG} = 2\text{ k}\Omega$, $C_{BAT} = 10\text{ }\mu\text{F}$

Figure 7. V_{BAT}

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Block Diagram

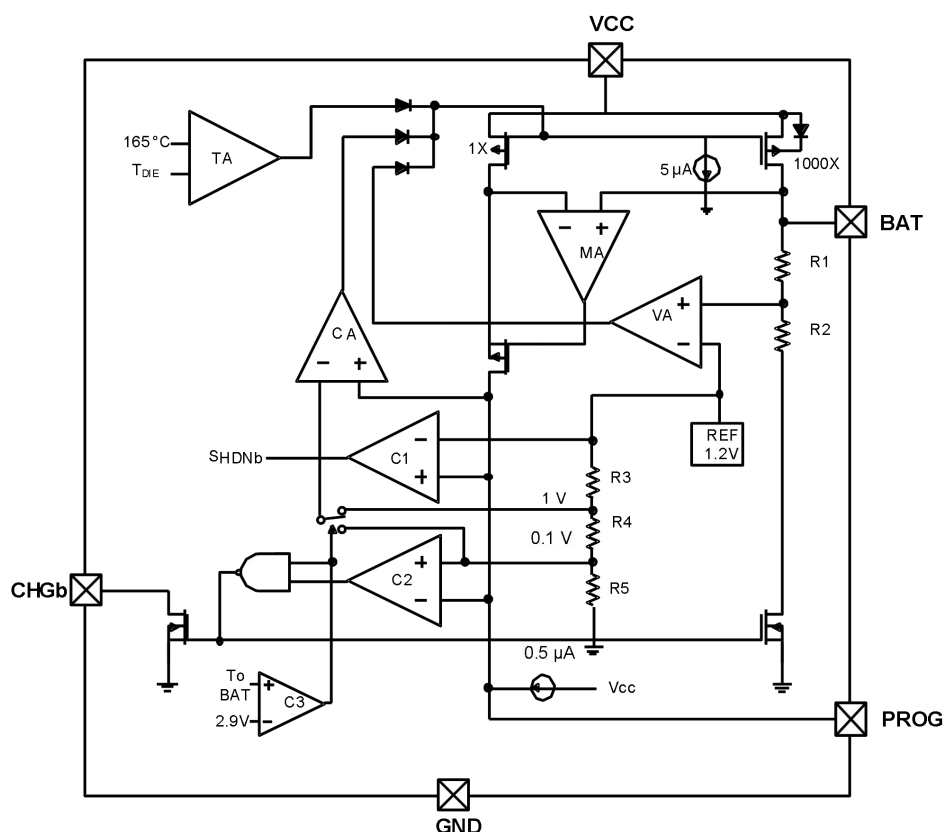


Figure 8. Functional block diagram

Operation information

The DIO5518B is a single cell Lithium-Ion battery charger using a constant-current / constant-voltage algorithm. It can deliver up to 100 mA of charge current (using a good thermal PCB layout) with a final float voltage accuracy of $\pm 1\%$. The DIO5518B includes an internal P-channel power MOSFET and thermal regulation circuitry. No blocking diode or external current sense resistor is required; thus, the basic charger circuit requires only two external components. Furthermore, the DIO5518B is capable of operating from a USB power source.

Normal charge cycle

A charge cycle begins when the voltage at the VCC pin rises above the UVLO threshold level and a 1% program resistor is connected from the PROG pin to ground or when a battery is connected to the charger output. If the BAT pin is less than 2.9 V, the charger enters trickle charge mode. In this mode, the DIO5518B supplies approximately 1/10 the programmed charge current to bring the battery voltage up to a safe level for full current charging.

When the BAT pin voltage rises above 2.9 V, the charger enters constant-current mode, where the programmed charge current is supplied to the battery. When the BAT pin approaches the final float voltage, the DIO5518B enters constant-voltage mode and the charge current begins to decrease. The charge cycle ends when the PROG voltage is less than 100 mV.

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Programming charge current

The charge current is programmed using a single resistor from the PROG pin to ground. The battery charge current of constant current mode is 1000 times the current out of the PROG pin. The program resistor and the charge current of constant current are calculated using the following equations:

$$I_{\text{CHRG}} = \frac{1V}{R_{\text{PROG}}} \times 1000 \quad (1)$$

Charge termination

A charge cycle is terminated when the charge current falls to 1/10 of the programmed value after the final float voltage is reached. This condition is detected by using an internal, filtered comparator to monitor the PROG pin. When the PROG pin voltage falls below 100 mV for longer than t_{TERM} (typically 1 ms), charging is terminated. The charge current is latched off and the DIO5518B enters standby mode, where the input supply current drops to 136 μA . (Note: CC/10 termination is disabled in trickle charging mode and thermal limiting modes).

When charging, transient loads on the BAT pin can cause the PROG pin to fall below 100 mV for short periods of time before the DC charge current has dropped to 1/10 of the programmed value. The 1 ms filter time (t_{TERM}) on the termination comparator ensures that transient loads of this nature do not result in premature charge cycle termination. Once the average charge current drops below 1/10 of the programmed value, the DIO5518B terminates the charge cycle and ceases to provide any current through the BAT pin, the chip will be put into standby mode. In this state, all loads on the BAT pin must be supplied by the battery.

The DIO5518B constantly monitors the BAT pin voltage in standby mode. If this voltage drops below the 4.05 V recharge threshold (V_{RECHRG}), another charge cycle begins and current is once again supplied to the battery. The state diagram of a typical charge cycle is as below:

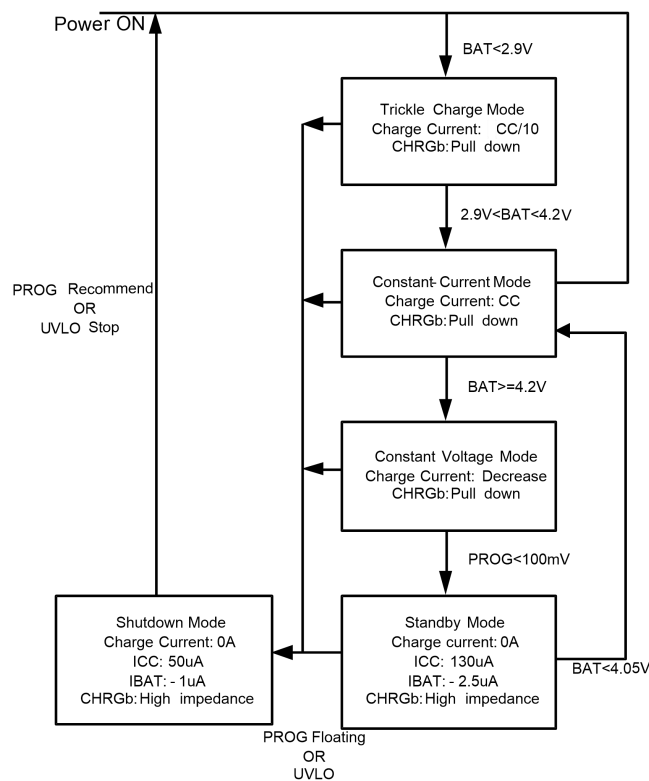


Figure 9. State diagram of a typical charge cycle

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Charge status indicator (CHGb)

The charge status output indicator is an open drain circuit. The indicator has two different states: pull-down (~ 10 mA), and high impedance. The pull-down state indicates that the DIO5518B is in a charge cycle. High impedance indicates that the charge cycle is complete. The CHGb also can be used to detect the charge states by a microprocessor with a pull-up resistor.

Shutdown mode

At any point in the charge cycle, the DIO5518B can be put into shutdown mode by removing R_{PROG} thus floating the PROG pin. This reduces the battery drain current to less than $0.5 \mu\text{A}$ and the supply current to less than $45 \mu\text{A}$. A new charge cycle can be initiated by reconnecting the program resistor. Below is a recommend application.

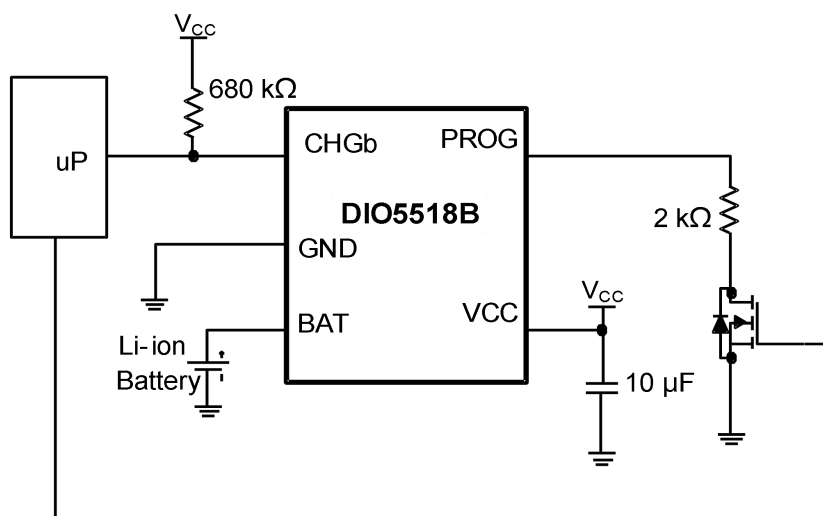


Figure 10. Manual shutdown mode application circuit

The DIO5518B also be put into shutdown mode when VCC voltage down to UVLO threshold or up to OVLO Threshold. In this state, the CHGb pin is high impedance state. The CHGb pin is also in a high impedance state if the charge cycle is completed.

Automatic recharge

Once the charge cycle is terminated, the DIO5518B continuously monitors the voltage on the BAT pin using a comparator with a 2 ms filter time (t_{RECHRG}). A charge cycle restarts when the battery voltage falls below 4.05 V (which corresponds to approximately 80% to 90% battery capacity). This ensures that the battery is kept at or near a fully charged condition and eliminates the need for periodic charge cycle initiations. CHGb output enters a pull-down state during recharge cycles.

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Application Information

Typical application

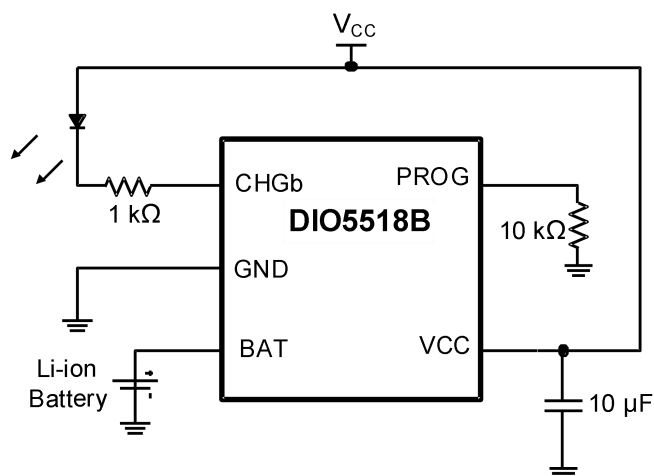


Figure 11. Typical applications W/T LED indicate

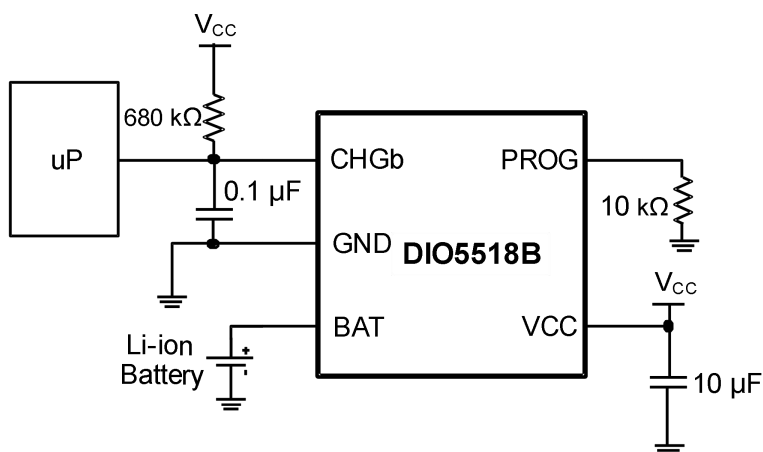


Figure 12. Typical applications W/T microprocessor detect

Stability considerations

The constant-voltage mode feedback loop is stable without an output capacitor provided a battery is connected to the charger output. With no battery present, an output capacitor is recommended to reduce ripple voltage. When using high value, low ESR ceramic capacitors, it is recommended to add a 1 Ω resistor in series with the capacitor. No series resistor is needed if tantalum capacitors are used.

In constant-current mode, the PROG pin is in the feedback loop, not the battery. The constant-current mode stability is affected by the impedance at the PROG pin. With no additional capacitance on the PROG pin, the charger is stable with program resistor values as high as 50 k Ω . However, additional capacitance on this node reduces the maximum allowed program resistor thus it should be avoided.

Thermal limit

An internal thermal feedback loop reduces the programmed charge current if the die temperature attempts to

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rise above a preset value of approximately 165°C. This feature protects the DIO5518B from excessive temperature and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the DIO5518B. The charge current can be set according to typical (not worst-case) ambient temperature with the assurance that the charger will automatically reduce the current in worst-case conditions.

Power dissipation

The conditions that cause the DIO5518B to reduce charge current through thermal feed-back can be approximated by considering the power dissipated in the IC. Nearly all of this power dissipation is generated by the internal MOSFET. This is calculated to be approximately:

$$P_D = (V_{CC} - V_{BAT}) \times I_{BAT} \quad (2)$$

It is important to remember that DIO5518B applications do not be designed for worst-case thermal conditions since the IC will automatically reduce power dissipation when the junction temperature reaches approximately 160°C (Constant temperature mode).

VCC bypass capacitor

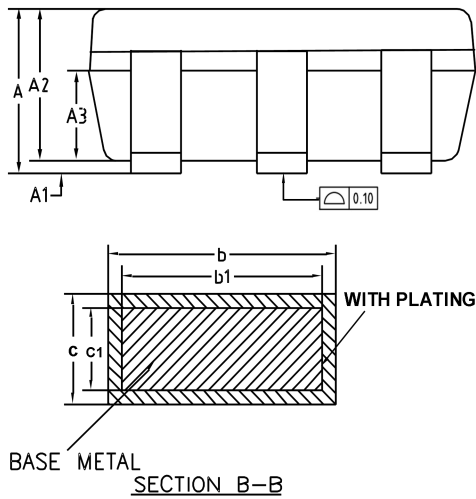
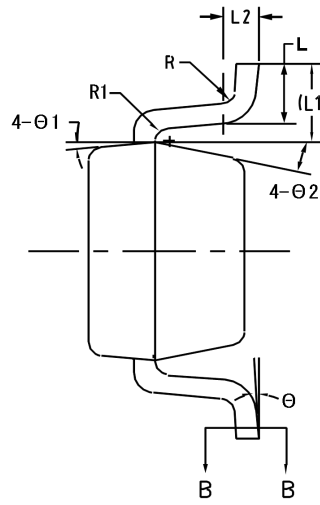
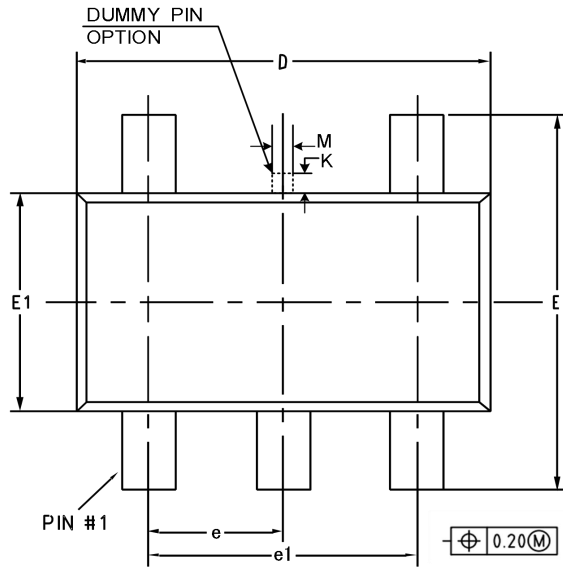
Many types of capacitors can be used for input bypass, however, caution must be exercised when using multilayer ceramic capacitors. Because of the self-resonant and high Q characteristics of some types of ceramic capacitors, a 10 µF ceramic capacitor is recommended for this bypass capacitor. Due to a high voltage transient will be generated under some start-up conditions, such as connecting the charger input to a live power source.

Charge current soft-start

The DIO5518B includes a soft-start circuit to minimize the inrush current at the start of a charge cycle. When a charge cycle is initiated, the charge current ramps from zero to the full-scale current over a period of approximately 100 µs. This has the effect of minimizing the transient current load on the power supply during start-up.

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Physical Dimensions: SOT23-5



Common Dimensions (Units of measure = Millimeter)			
Symbol	Min	Nom	Max
A	-	-	1.25
A1	0	-	0.15
A2	1.00	1.10	1.20
A3	0.60	0.65	0.70
b	0.36	-	0.45
b1	0.35	0.38	0.41
c	0.14	-	0.20
c1	0.14	0.15	0.16
D	2.826	2.926	3.026
E	2.60	2.80	3.00
E1	1.526	1.626	1.726
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
K	0	-	0.25
L	0.30	0.40	0.60
L1	0.59 REF		
L2	0.25 BSC		
M	0.10	0.15	0.25
R	0.05	-	0.20
R1	0.05	-	0.20
θ	0°	-	8°
θ1	8°	10°	12°
θ2	10°	12°	14°

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